





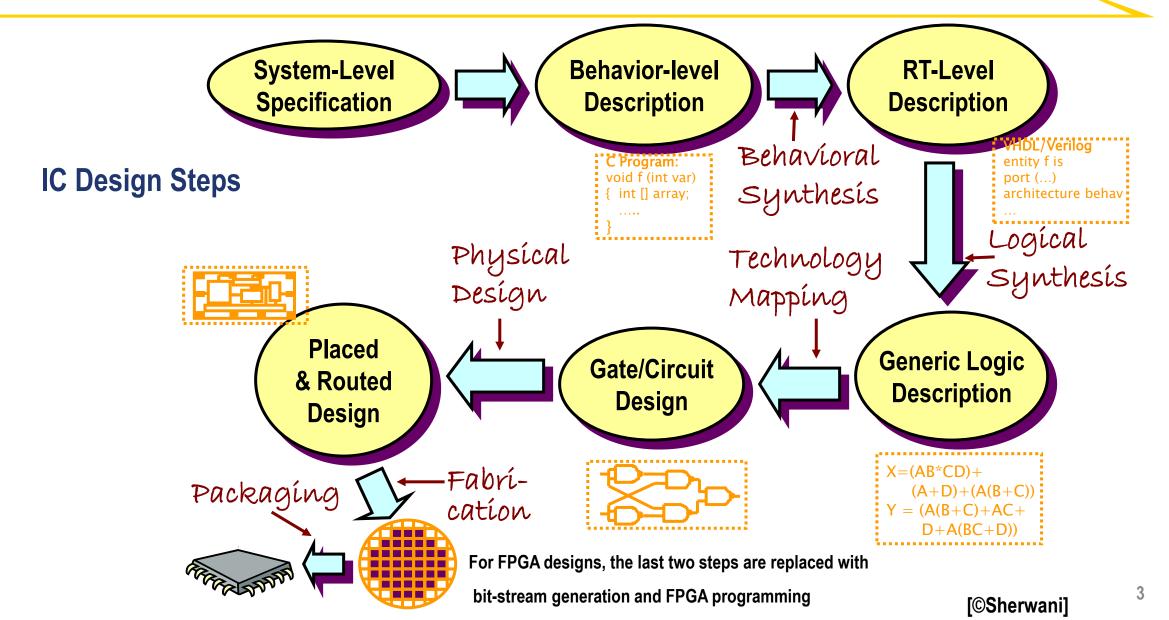
Qubit Mapping and Scheduling:

Gap Analysis and Optimal Solutions

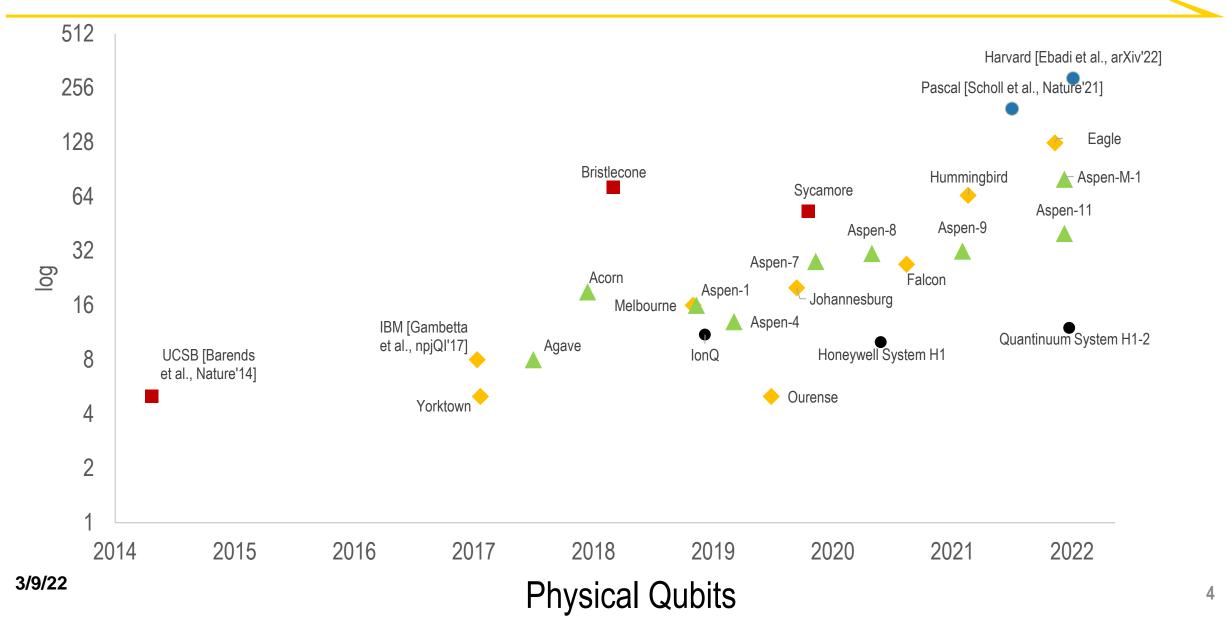
Jason Cong UCLA Computer Science PhD Student: Daniel Bochen Tan

VLSI Design Automation Flow

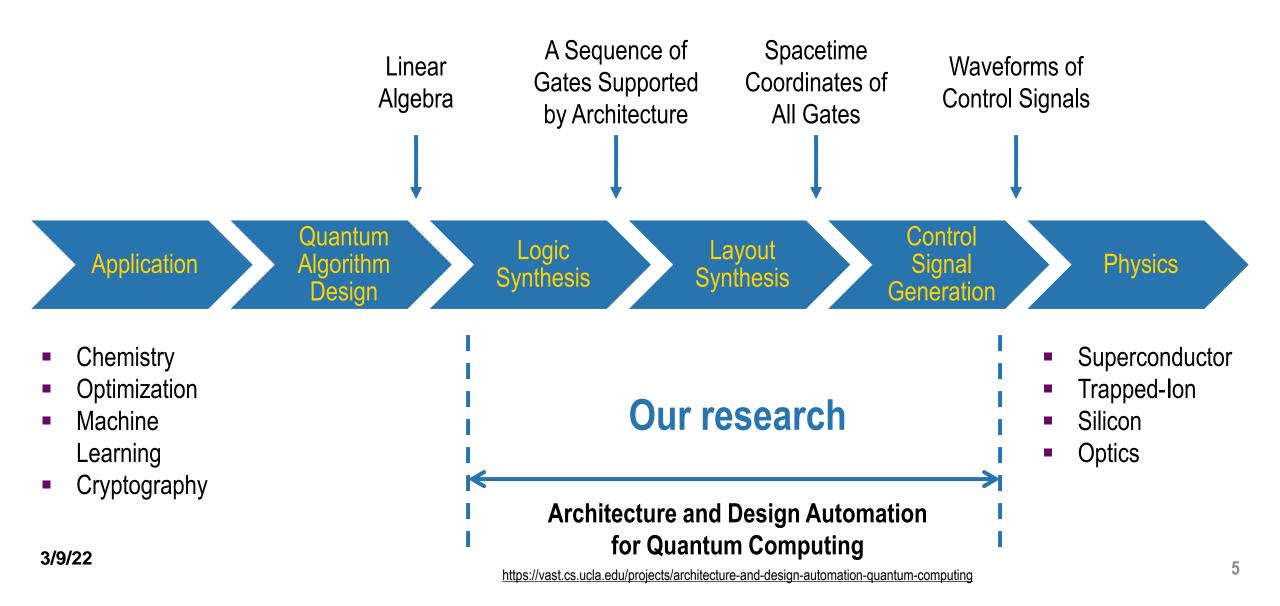
3/9/22



Advances of Quantum Computing



Flow of Quantum Computing



Gate Model of Quantum Computation: Quantum Gates

Single-qubit gate

• "Bitflip" gate
$$X: \begin{pmatrix} \alpha \\ \beta \end{pmatrix} \mapsto \begin{pmatrix} \beta \\ \alpha \end{pmatrix}$$
 i.e., $\begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}$

- Hadamard gate H = \frac{1}{\sqrt{2}} \binom{1}{1} & 1 \\ 1 & -1 \binom{1}{2}\$
 Phase shift gate R_\phi = \binom{1}{0} & e^{i\phi} \binom{1}{2}\$

- Two-qubit gate
 - Controlled-not gate *CX*:

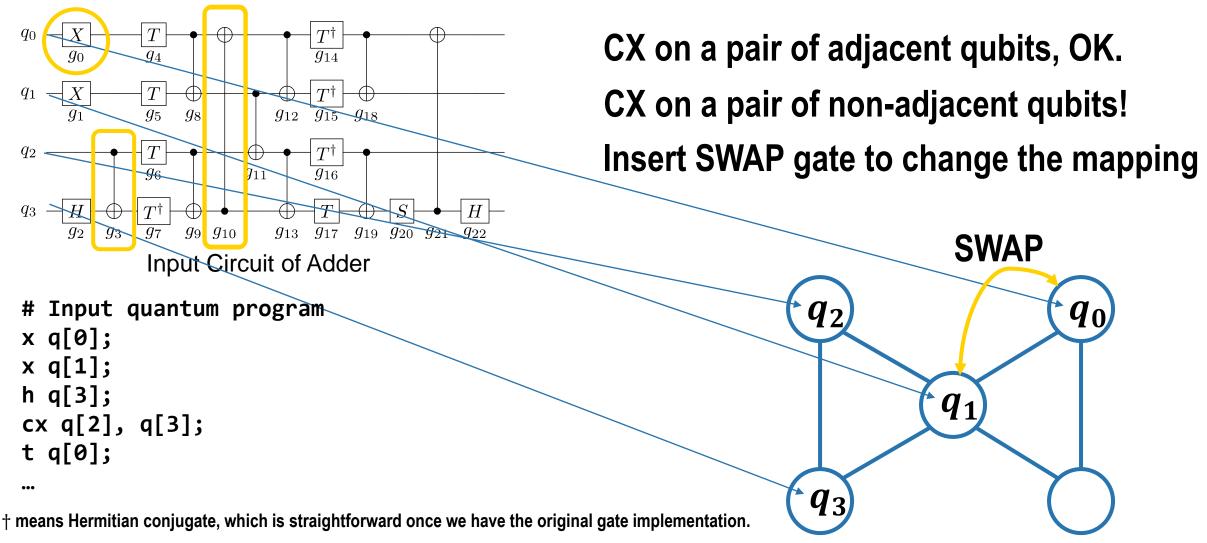
$$\begin{pmatrix} \alpha \\ \beta \\ \gamma \\ \delta \end{pmatrix} \mapsto \begin{pmatrix} \alpha \\ \beta \\ \delta \\ \gamma \end{pmatrix}$$

•
$$P \equiv R_{\frac{\pi}{2}}, T \equiv R_{\frac{\pi}{4}}$$

• Qiskit U_3 gate $U3(\theta, \phi, \lambda) = \begin{pmatrix} \cos(\theta/2) & -e^{i\lambda}\sin(\theta/2) \\ e^{i\phi}\sin(\theta/2) & e^{i(\phi+\lambda)}\cos(\theta/2) \end{pmatrix}$

- All gates are unitary: AA⁺ = I
- **The Solovay-Kitaev Theorem:** the gate set {*H*, *P*, *T*, *CX*} is universal for quantum computing! [Nielsen&Chuang, QCQI]

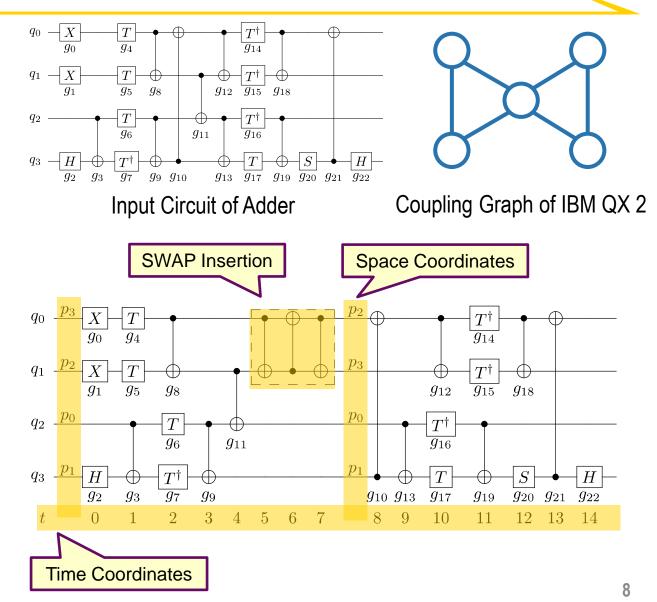
Qubit Mapping and Scheduling (Layout Synthesis) for QC (LSQC)



7

Layout Synthesis for Quantum Computing (LSQC)

- Input: quantum circuit/program, coupling graph
- Output: spacetime coordinates of all gates, including inserted SWAPs
- Objectives: depth, additional SWAP count, fidelity, ...
- Constraints:
 - Execute all gates
 - Respect dependencies





Introduction

- Gap analysis for quantum compilation
- Optimal layout synthesis for quantum computing (OLSQ)
- OLSQ with Gate Absorption

Previous Works on Layout Synthesis for Quantum Computing

Layer-by-layer:

- [Maslov et al., TCAD'08], [Zulehner et al., DATE'18]: lookahead search guided by heuristic cost function
- [Shafaei et al., ASPDAC'14]: optimize the 'total distance'

• Gate-by-gate:

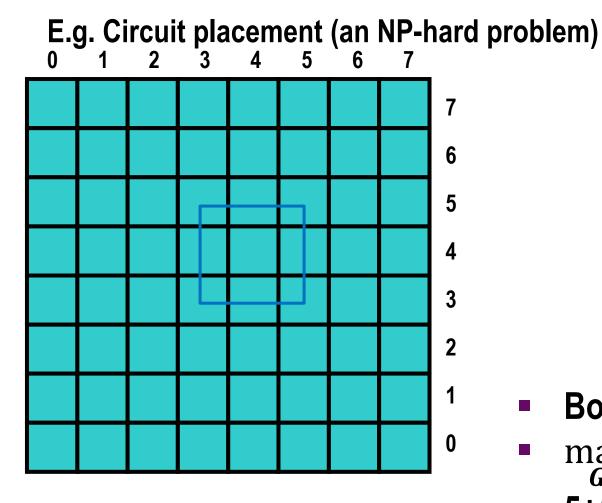
- [Siraichi et al., CGO'18]: heuristic search for min #SWAPs
- [Wille et al., DAC'19]: optimize #SWAPs

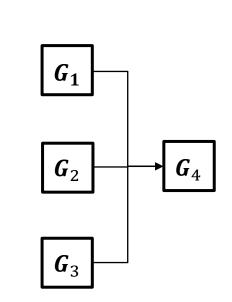
Use dependency:

- [Murali et al., ISCA'19]: optimize fidelity upper bound
- [Li et al., ASPLOS'19]: bi-directional search with cost function concerning both #SWAPs and depth
- Industry tools: Quilc, Qiskit, t|ket>, Cirq, ...

Are they good enough?

Similar Questions Have Been Asked Before for VLSI Designs





Bounding box wire-length

max
$$x_G + \max_G y_G - \min_G x_G - \min_G y_G$$
5+5-3-3 = 4

Construction of Placement Examples with Known Optimal (PEKO) Wirelength [Chang et al., TCAD'04]

- Up to 2 million placeable objects
 - Initial WL gap: 1.6x 2.5x (2003)
- Multiple EE Times articles coverage, e.g.
 - Placement tools criticized for hampering IC designs [Feb'03]
- Many downloads from our website
 - Cadence, IBM, Intel, Magma, Mentor Graphics, Synopsys, ...
 - CMU, MIT, SUNY, UCB, UCSB, UCSD, UIC, UMichgan, UWaterloo, ...
- Optimality gap on PEKO was narrowed down to ~20% as of 2007 (from 60% - 150%)
- Improvement on real circuits as well
 - 30+% improvement by mPL placer 2003-06



http://cadlab.cs.ucla.edu/~pubbench

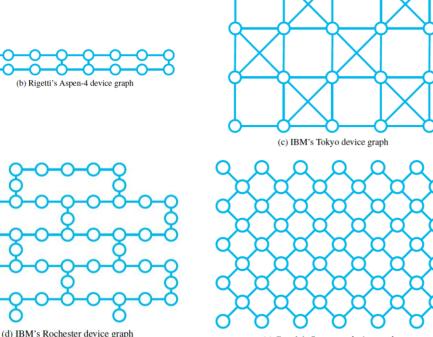
Quantum Mapping Examples with Known Optimal (QUEKO)

QUEKO: depth and gate count optimal benchmarks tailored to arbitrary devices for LSQC

- Input: device graph, target depth, gate density
- Backbone construction: grow a dependency chain
- Sprinkling: match the gate density profile
- Scrambling: challenge the LSQC tools
- Output: OpenQASM file

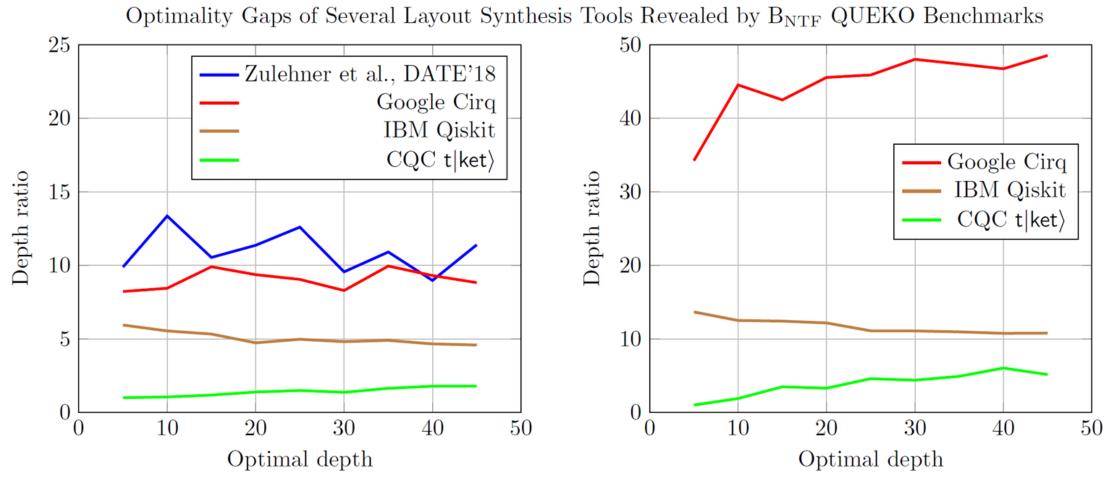
Evaluating Existing LSQC Tools with QUEKO

- Devices: Google Sycamore, Rigetti Aspen-4, IBM Q Tokyo, and IBM Q Rochester
- Circuits: QUEKO benchmarks
 - Depth:
 - ✤ 5-45 as near-term feasible,
 - ✤ 100-900 as scalability study
 - Gate density: profile of Toffoli gate and quantum supremacy experiment [Arute et al., Nature'19]
- Tools:
 - Cirq (Google)
 - Qiskit (IBM)
 - tket (Cambridge QC, now Quantinuum)
 - [Zulehner et al., DATE'18]



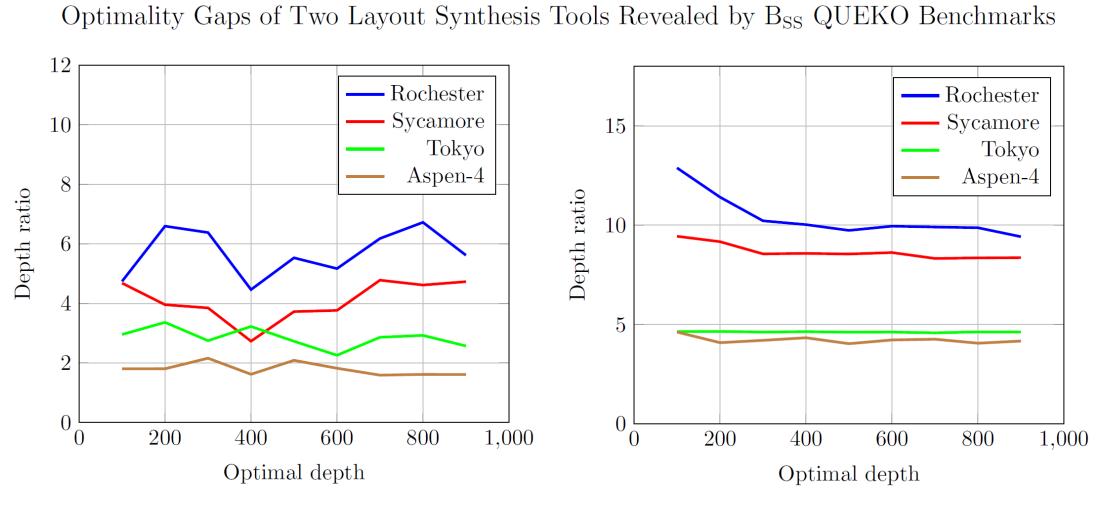
(e) Google's Sycamore device graph

QUEKO Results: Near-Term Feasible



Toffoli gate density Rigetti Aspen-4 Device Quantum supremacy experiment gate density Google Sycamore device

QUEKO Results: Scalability Study



CQC t $|ket\rangle$ Performance

3/9/22

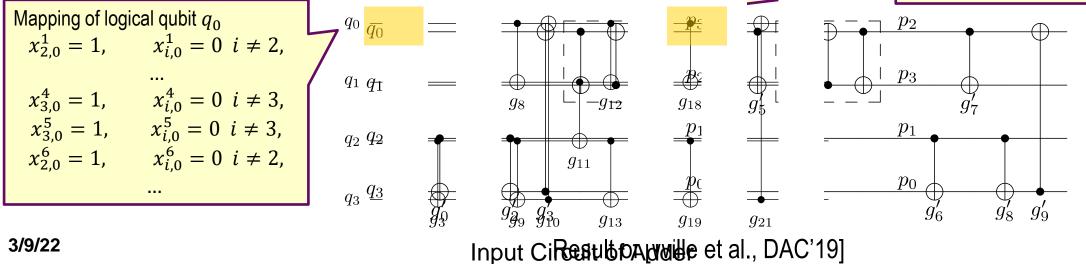
IBM Qiskit Performance

Introduction

- Gap analysis for quantum compilation
- Optimal layout synthesis for quantum computing (OLSQ)
- OLSQ with Gate Absorption

Large Solution Space of LSQC

- [Wille et al., DAC'19]: L₂ two-qubit gates, M logical qubits, N physical qubits
 - Mapping $x_{pq}^{l} = 1$ iff. logical qubit q is mapped to physical qubit p before gate g'_{l}
 - 2^{L_2MN} search space for mapping
 - Permutation variables $y_{\Pi}^{l} = 1$ iff. before gate g_{I}^{\prime} , qubits have a permutation Π
 - $L_2 N!$ permutation variables. Needs to pre-compute min cost of each Π . The SWAP insertion y_{Π}^4 and $y_{\Pi}^6 = 1$ where $\Pi: 0 \mapsto 0, 1 \mapsto 1, 2 \mapsto 3, 3 \mapsto 2$
 - The example of adder below: >1,500 variables



 q_1

q₀

 p_2

 p_0

 q_3

 (q_2)

For all other Π 's and *l*'s, $y_{\Pi}^{l} = 0$

Our Approach: OLSQ (Optimal Layout Synthesis for Quantum Computing)

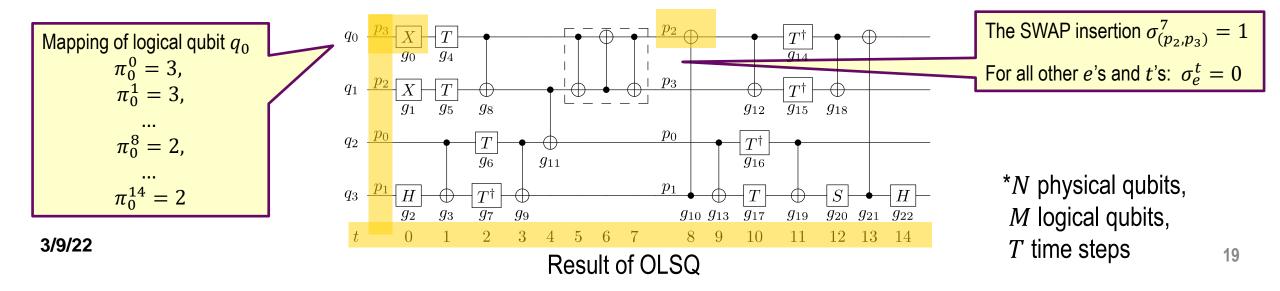
 q_0

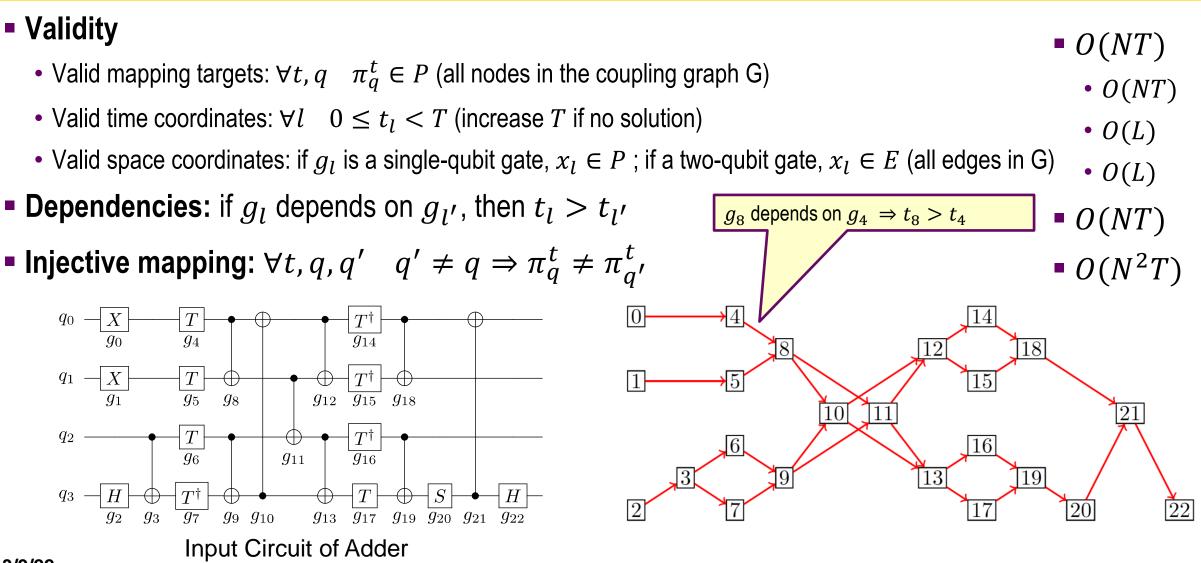
 (q_1)

 p_2

Variables in OLSQ

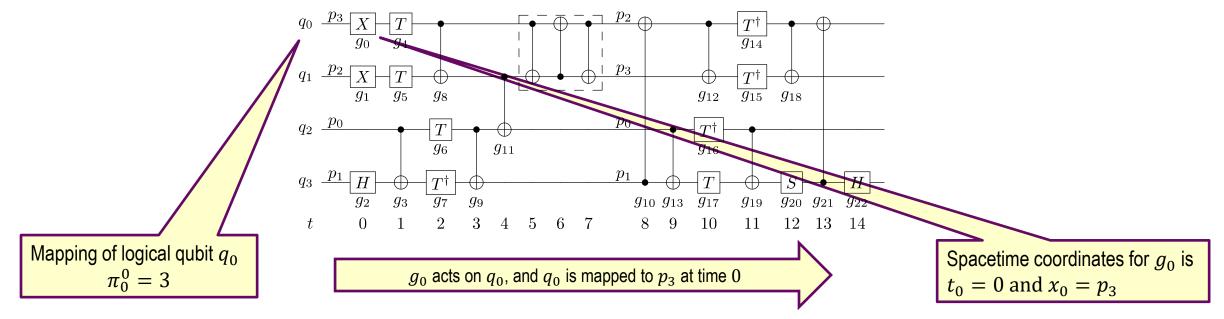
- Spacetime Coordinates (x_l, t_l) for every gate g_l
 - ✤ If g_l is a single-qubit gate, x_l is a physical qubit; if g_l is a two-qubit gate, x_l is an edge
- Mapping π_q^t : at time t, logical qubit q is mapped to the physical qubit π_q^t
- Use of SWAP σ_e^t : $\sigma_e^t = 1$ iff. there is a SWAP on edge e and its last time step is t
- More efficient encoding of search space*: N^{MT}





- Mapping implies spacetime coordinates
 - $(t_0 = 0 \land x_0 = p_3) \Rightarrow (\pi_0^0 = p_3)$





Legal SWAPs:

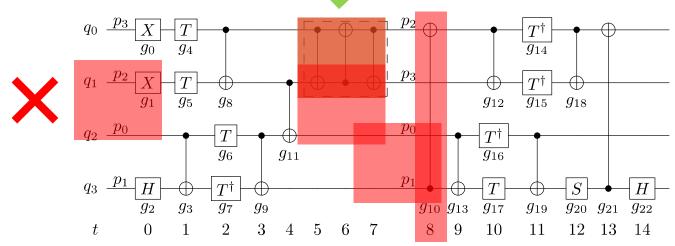
• Initial SWAP conditions

 $\bigstar 0 \le t < 3, \forall e \quad \sigma_e^t = 0$

• No overlaps between SWAPs

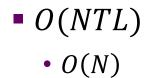
$$\bigstar \left(\sigma_{(p_2,p_3)}^7 = 1 \right) \Rightarrow \left(\sigma_{(p_2,p_0)}^7 = 0 \right)$$

• No overlaps between SWAPs and original gates



 p_0

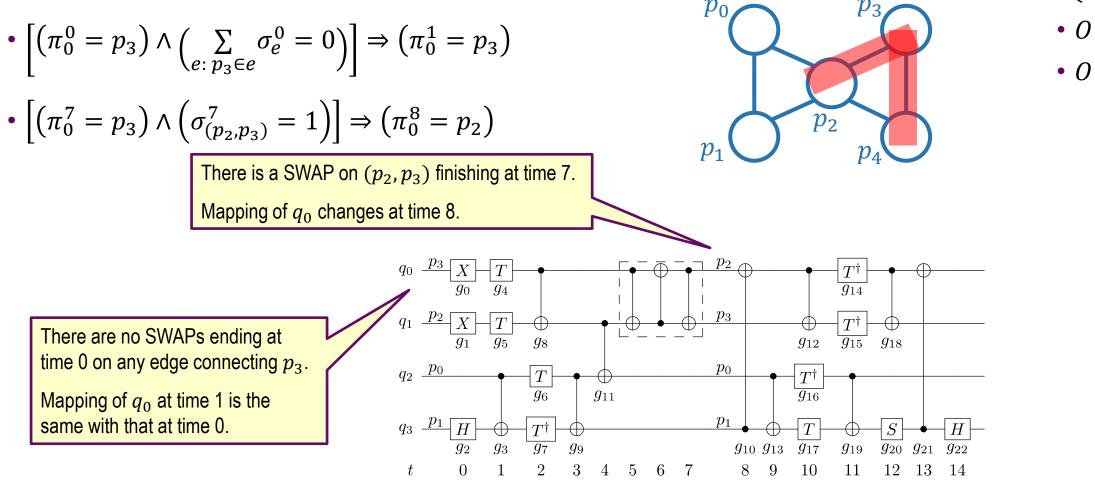
 p_2



• *O*(*NT*)

• *O*(*NTL*)

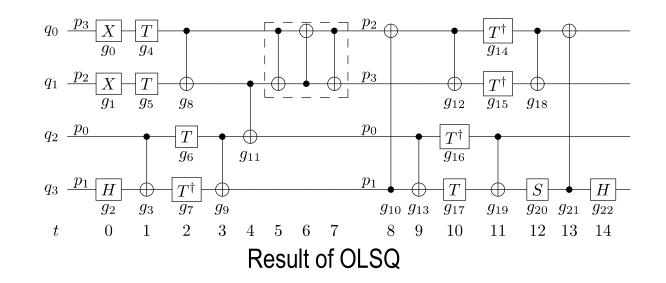
Mapping transformed by SWAPs



• $O(N^2T)$ • $O(N^2T)$ • $O(N^2T)$

OLSQ Optimization Objectives

- Depth = max t_l
- **•**#SWAP = $\sum \sigma_e^t$, or/and
- **Fidelity =** $\prod_{q} f_{\mathrm{m}}(\pi_{q}^{T}) \cdot \prod_{l_{1}} f_{1}(x_{l_{1}}) \cdot \prod_{l_{2}} f_{2}(x_{l_{2}}) \cdot \prod_{e,t} f_{S}(e)^{\sigma_{e}^{t}}$
 - $f_{\rm m}$, f_1 , f_2 , and f_s are measurement, single-qubit gate, two-qubit gate, and SWAP fidelity.
 - π_q^T is the final mapping. l_1 goes over all single-qubit gates; l_2 goes over all two-qubit gates.



Solved Using SMT (Satisfiability Modulo Theories)

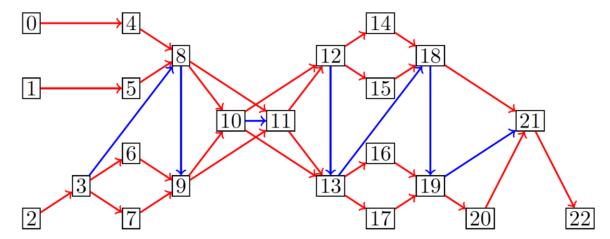
- SAT (Boolean Satisfiability): given a conjunctive normal form, whether there is an assignment such that it is true. E.g.,
 - $a \wedge (\overline{a} \lor b) \wedge c$
 - Solution: a = b = c = True
- SMT generalizes SAT to more complex formulas involving real numbers, integers, lists, arrays, bit-vectors, etc. E.g.
 - $a \coloneqq x + y < 3$, $b \coloneqq x < 4 y$, $c \coloneqq x > 0$.
 - Then, x = y = 1 makes the model satisfiable.
- SMT is very expressive, widely used in compilation, programming language, formal verification, etc.
- There are efficient SMT solvers, such as Z3 (and we can further customize for OLSQ)

Summary of Constraints for OLSQ

| Constraints | OLSQ |
|--|-----------|
| Validity | O(NT) |
| Injective Mapping | $O(N^2T)$ |
| Dependency | O(NT) |
| Mapping constrains Spacetime Coordinates | O(NTL) |
| No Overlap with Other SWAPs | O(NT) |
| No Overlap with Original Gates | O(NTL) |
| Mapping transformed by SWAPs | $O(N^2T)$ |
| In total | O(NTL) |

O(NTL)

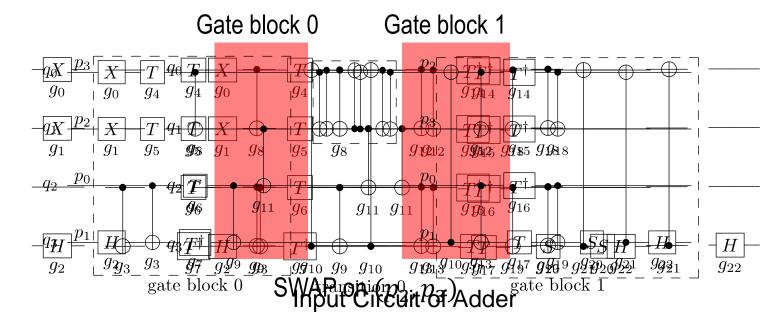
- Efficiency: O(NT) vars & O(NTL) constraints versus $O(L_2N!)$ & $O(L_2MN!)$
- Complexity result: a polynomial certificate \rightarrow quantum LS is in NP
- Optimality: independent from input gate order



Side-effect o Departen bly-rogaites perofcerse in the yin QVASICe et al., DAC'19]

Transition-Based OLSQ

- Motivation: many mapping variables are redundant in the lack of SWAPs.
- Solution: gate blocks + transitions.
- Variables: mapping, spacetime, SWAP for each block instead for each time step
 - 2 blocks versus 14 time steps
- After SWAP insertion, we can use ASAP (as soon as possible) scheduling



Constraints for TB-OLSQ

| Constraints | TB-OLSQ Revision |
|--|------------------|
| Validity | |
| Injective Mapping | |
| Dependency | |
| Mapping constrains Spacetime Coordinates | |
| No Overlap with Other SWAPs | |
| No Overlap with Original Gates | |
| Mapping transformed by SWAPs | |

Summary of Constraints for TB-OLSQ

| Constraints | OLSQ | [Wille et al., DAC'19] |
|--|-----------|------------------------|
| Validity | O(NT) | |
| Injective Mapping | $O(N^2T)$ | |
| Dependency | O(NT) | |
| Mapping constrains Spacetime Coordinates | O(NTL) | |
| No Overlap with Other SWAPs | O(NT) | |
| No Overlap with Original Gates | O(NTL) | |
| Mapping transformed by SWAPs | $O(N^2T)$ | |
| In total | O(NTL) | $O(L_2MN!)$ |

Comparison with OLSQ >400x speedup (geomean)

Benchmarks

Small circuits to verify optimality Larger arithmetic circuits QUEKO circuits

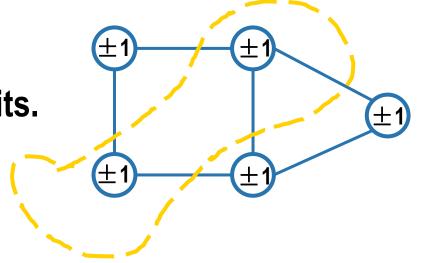
A more recent work [Zhang et al., ASPLOS'21] uses A* search with an admissible heuristic, which runs faster with depth-optimal solutions (but cannot optimize other objectives, e.g. fidelity).

Quantum Approximate Optimization Algorithm (QAOA)

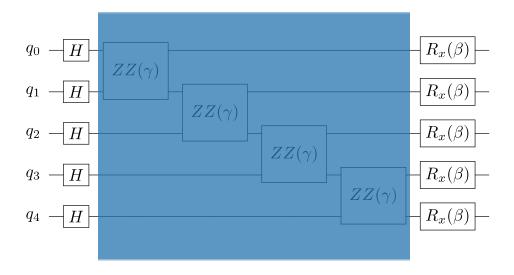
- Aiming optimization with binary variables
- Quantize the problem by changing variables to qubits.
- Example: MAX-CUT problem on G = (V, E)
- Assign ± 1 variables z_i to vertices

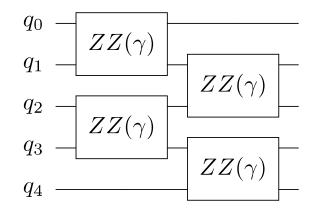
• MAX-CUT = Maximize
$$\sum_{(v_j, v_k) \in E} \frac{1 - z_j z_k}{2}$$

• $z_j z_k$ has a corresponding two-qubit gate, ZZ-Phase.



Commutable, i.e., AB=BA, since diagonal

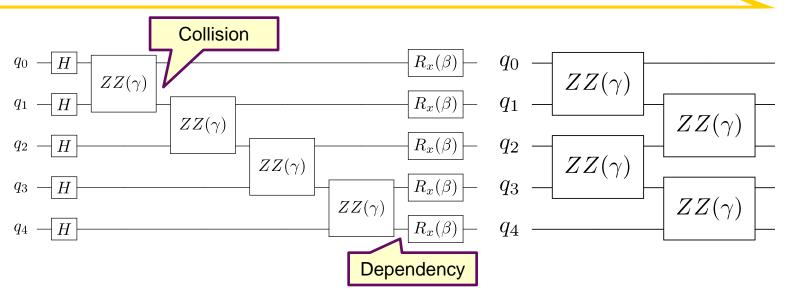




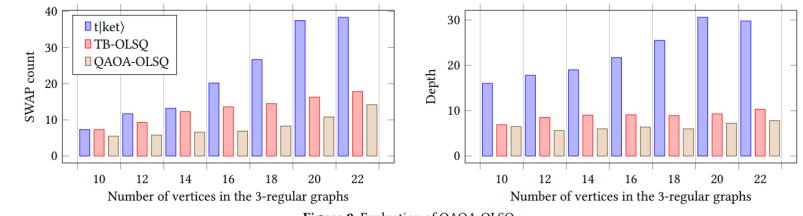
QAOA-OLSQ

3/9/22

 Observation: some 'dependencies' are not real, according to commutation.



- Solution: make a distinction between dependency and collision
- Result: 70% depth reduction, 54% SWAP reduction compared to tket.



Introduction

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Programmable Single-Qubit Gate -> Programmable Two-Qubit Gate

• A programmable single-qubit gate can be configured to be any matrix in U(2)

$$V(\theta,\phi,\lambda) = \begin{pmatrix} \cos(\theta/2) & -e^{i\lambda}\sin(\theta/2) \\ e^{i\phi}\sin(\theta/2) & e^{i(\phi+\lambda)}\cos(\theta/2) \end{pmatrix}$$

- Native two-qubit gate: CX
- A programmable two-qubit gate can be configured to any matrix in U(4)
- KAK Decomposition [Vatan&Williams, PRA'04]: any U(4) to 3 CX's and some U(2)

Quantum Programs as Lists of Programmable Two-Qubit Gates

π/2,

π/4)

- Driving applications
 - Chemistry simulation [Kivlichan et al., PRL'18]
 - Quantum Approximate Optimization Algorithm (QAOA) [Farhi et al., arXiv'14, Harrigan et al., NatPhys'21]
 - Quantum neural networks (QCNN) [Cong et al., NatPhys'19]

$$fSim(\theta, \phi) = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & \cos\theta & -i\sin\theta & 0 \\ 0 & -i\sin\theta & \cos\theta & 0 \\ 0 & 0 & 0 & e^{-i\phi} \end{bmatrix}$$

 $fSim(\frac{\pi}{4}, \frac{\pi}{4})$ can be decomposed as $V(\pi/2, \pi/2,$ *V*(π/2, *V*(π/4, *V*(π/8, $-3\pi/4$) -3π/8, -π/2, 0, π/2) -π/2) -π/2) *V*(π/2, V(2.38, V(1.05, V(2.38,

0.648,

-2.56)

-3.12,

1.6)

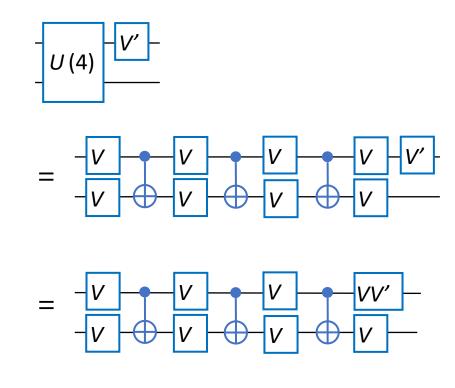
-1.94,

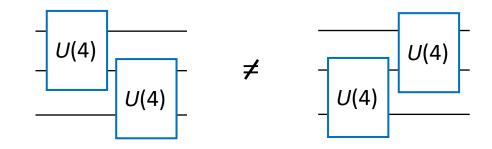
-π)

Take Advantage of Gate Absorption and Commutation

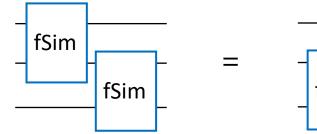
Gate absorption

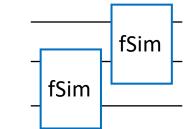
Dependency: relative order of the gates



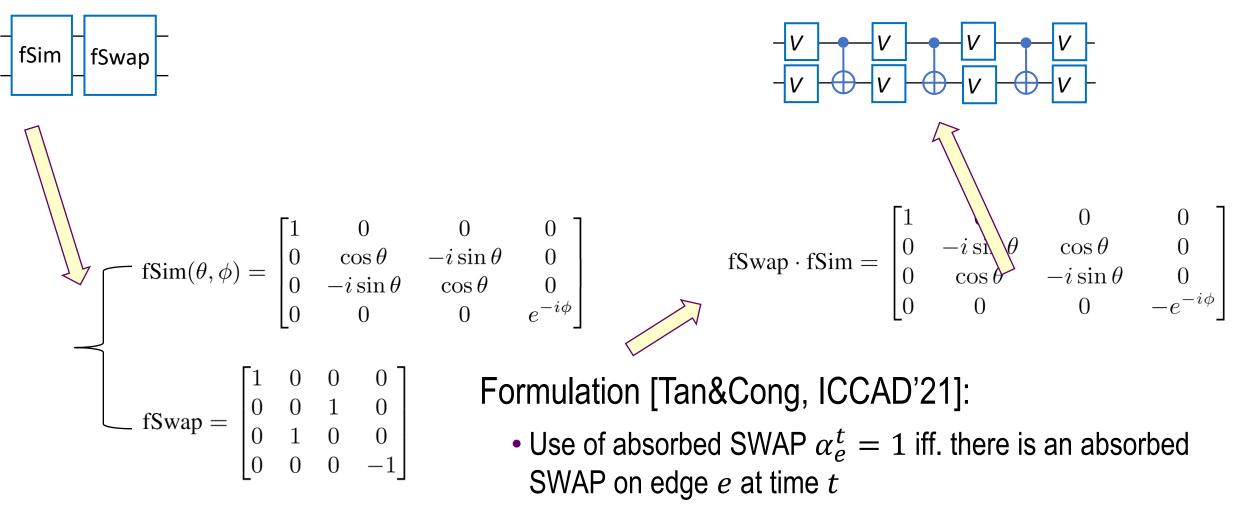


Gate commutation:





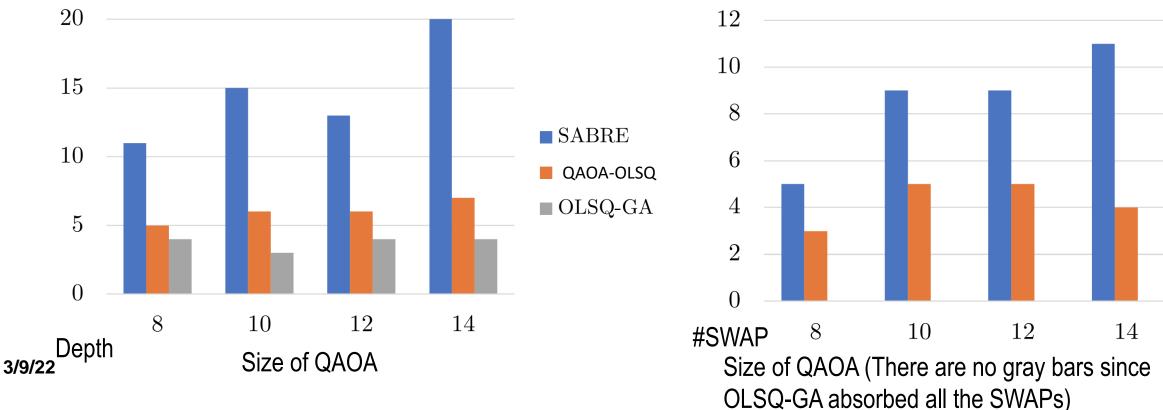
Two-Qubit Gate Absorption



- Mapping transformed by both absorbed and explicit SWAPs α_e^t and σ_e^t

QAOA Results with Gate Absorption (Depth and # Swap Gates)

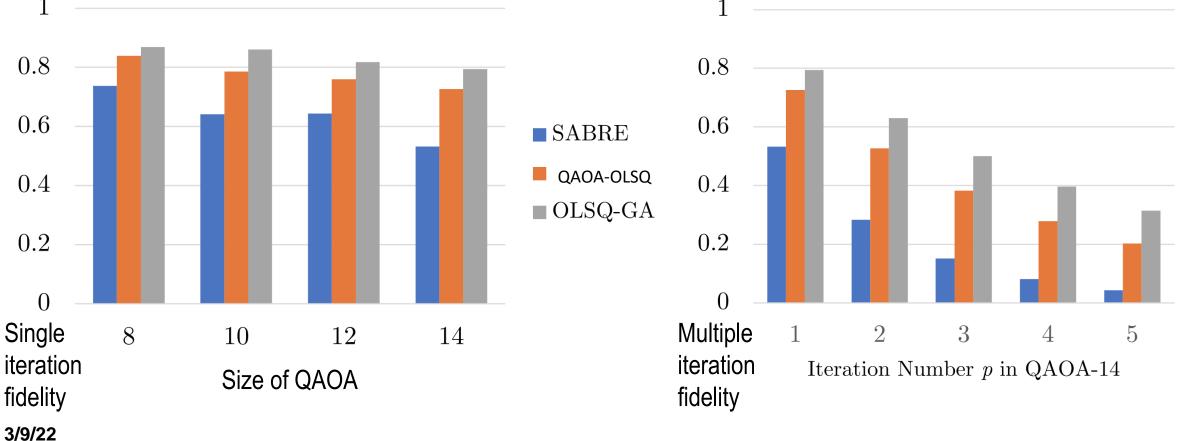
- Similar QAOA instances of size 8 to 14 like in [Harrigan et al., NatPhys'21]
- SABRE [Li et al., ASPLOS'19]: leading heuristic mapper, recently adopted in Qiskit
- OLSQ-GA (considers commutation) reduced depth up to 80%, absorbed all the SWAPs



40

QAOA Results with Gate Absorption (Fidelity)

- Fidelity estimated with slightly optimistic parameters $T_0 = 50$ and $f_{U(4)} = 99\%$
- OLSQ-GA improves fidelity by up to 49% for 1 iteration, 636% for 5 iterations.



Concluding Remarks

- There are significant advances in quantum computing (QC) device technology
- There is a great need for better design automation or compilation tools for QC
 - As measured by the QUEKO circuits

Optimization objectives for NISQ applications

- Circuit depth (decoherence time)
- Overall fidelity
- Scalability
- OLSQ provides a framework for optimal solution for layout synthesis
- Further opportunities to combine layout synthesis with logic synthesis
- A lot of more opportunities for compilation/design automation for QC on novel architectures 3/9/22

Acknowledgements

Supports from the Industrial Partners of the Center for Domain-Specific Computing (CDSC)

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