



Tutorial on QuantumFlow+VACSEN: A Visualization System for Quantum Neural Networks on Noisy Quantum Devices

Session 2: QuantumFlow Co-Design Framework

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Assistant Professor

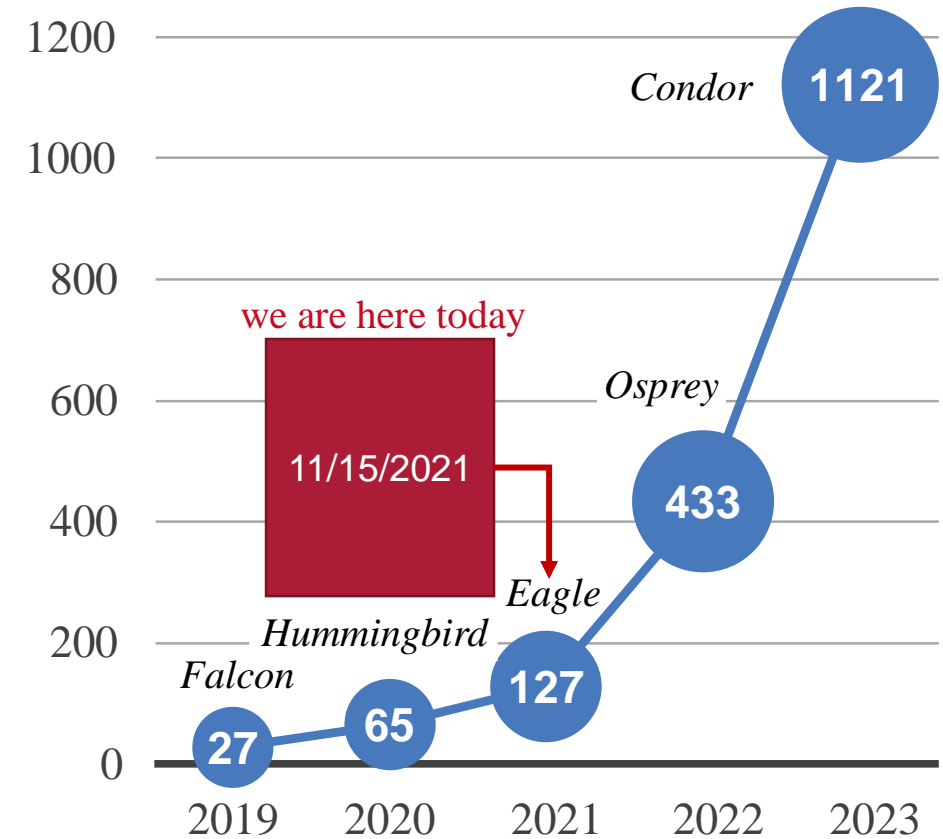
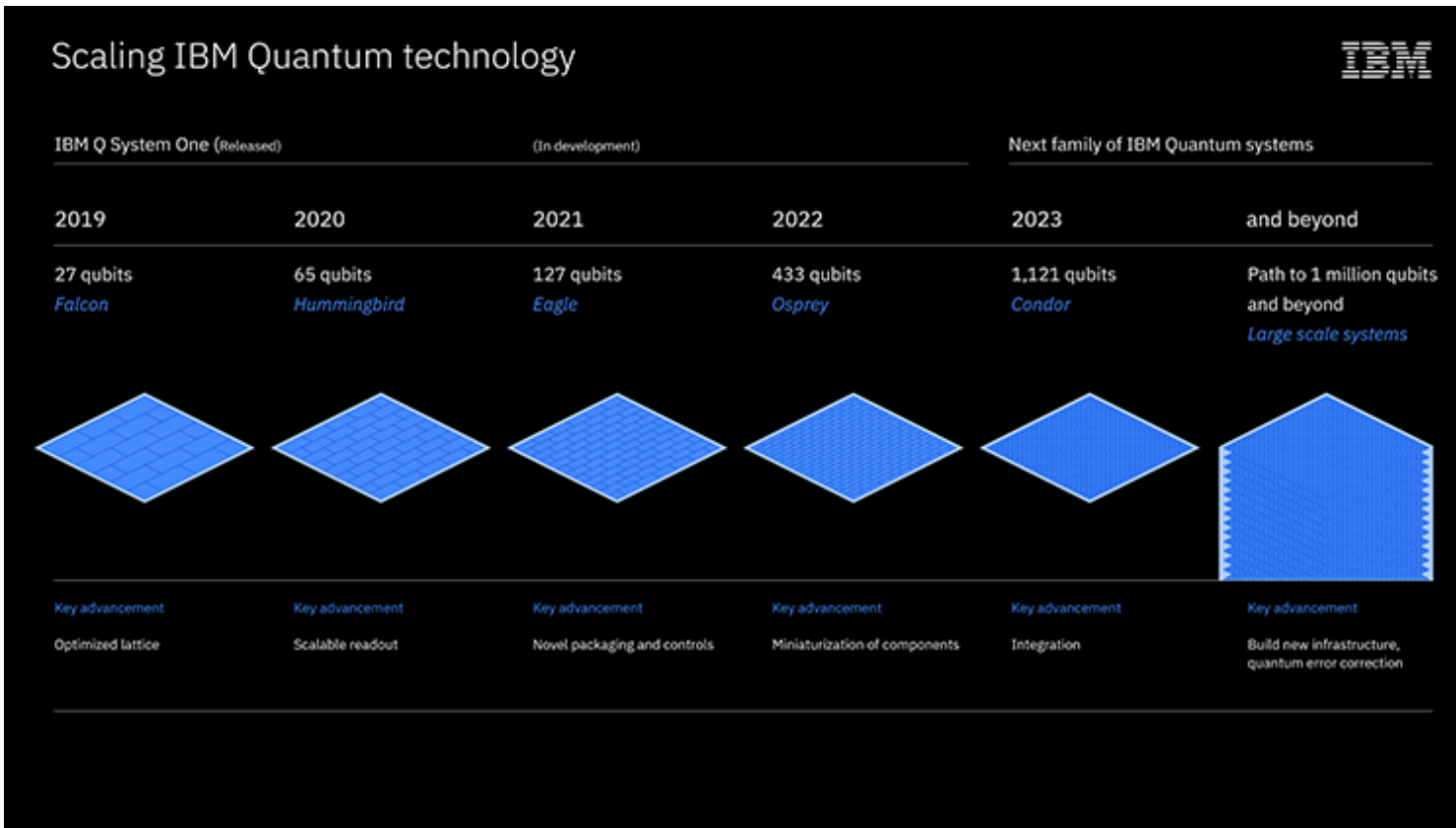
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Consistently Increasing Qubits in Quantum Computers



The Power of Quantum Computers: Qubit

Classical Bit

$$X = 0 \text{ or } 1$$

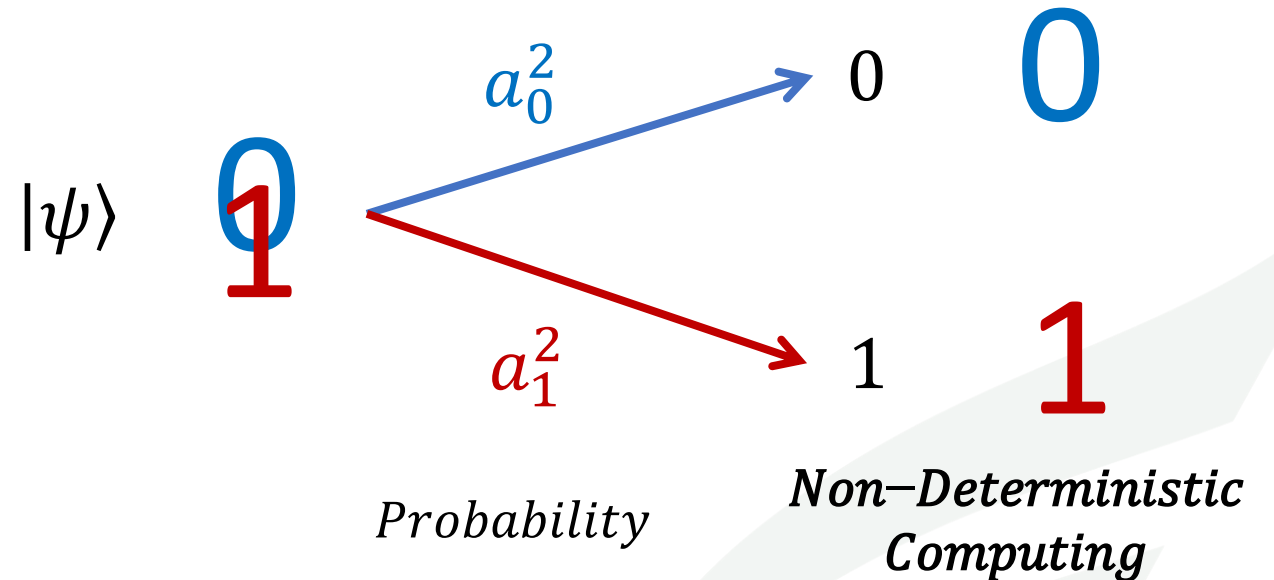
Quantum Bit (Qubit)

$$|\psi\rangle = |0\rangle \text{ and } |1\rangle$$

$$|\psi\rangle = a_0|0\rangle + a_1|1\rangle$$

$$\text{s. t. } a_0^2 + a_1^2 = 100\%$$

Reading out Information from Qubit (Measurement)



$$a_0^2 + a_1^2 = 100\%$$
$$40\% + 60\% = 100\%$$

The Power of Quantum Computers: Qubit

Classical Bit

$$X = 0 \text{ *or* } 1$$

Representation:

$$|\psi\rangle = a_0|0\rangle + a_1|1\rangle = \begin{pmatrix} a_0 \\ a_1 \end{pmatrix}$$

Quantum Bit (Qubit)

$$|\psi\rangle = |0\rangle \text{ *and* } |1\rangle$$

$$|\psi\rangle = a_0|0\rangle + a_1|1\rangle$$

$$\text{s. t. } a_0^2 + a_1^2 = 100\%$$

Initially:

$$|\psi\rangle = |0\rangle$$

, where $a_0 = 1$ and $a_1 = 0$

$$|\psi\rangle = |0\rangle = \begin{pmatrix} 1 \\ 0 \end{pmatrix}$$

The Power of Quantum Computers: Qubits

2 Classical Bits

00 **or** 01 **or** 10 **or** 11

n bits for 1 value
 $x \in [0, 2^n - 1]$

2 Qubits

$c_{00}|00\rangle$ **and** $c_{01}|01\rangle$ **and**
 $c_{10}|10\rangle$ **and** $c_{11}|11\rangle$

n bits for 2^n values
 $a_{00}, a_{01}, a_{10}, a_{11}$

Qubits: q_0, q_1

$$|q_0\rangle = a_0|0\rangle + a_1|1\rangle$$

$$|q_1\rangle = b_0|0\rangle + b_1|1\rangle$$

$$|q_0, q_1\rangle = |q_0\rangle \otimes |q_1\rangle$$

$$= c_{00}|00\rangle + c_{01}|01\rangle + c_{10}|10\rangle + c_{11}|11\rangle$$

- $|00\rangle$: Both q_0 and q_1 are in state $|0\rangle$
- c_{00}^2 : Probability of both q_0 and q_1 are in state $|0\rangle$
- $c_{00}^2 = a_0^2 \times b_0^2$
- $c_{00} = \sqrt{a_0^2 \times b_0^2} = a_0 \times b_0$

The Power of Quantum Computers: Qubits

2 Classical Bits

00 **or** 01 **or** 10 **or** 11

n bits for 1 value
 $x \in [0, 2^n - 1]$

2 Qubits

$c_{00}|00\rangle$ **and** $c_{01}|01\rangle$ **and**
 $c_{10}|10\rangle$ **and** $c_{11}|11\rangle$

n bits for 2^n values
 $a_{00}, a_{01}, a_{10}, a_{11}$

Qubits: q_0, q_1

$$|q_0\rangle = a_0|0\rangle + a_1|1\rangle$$

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$$|q_0, q_1\rangle = |q_0\rangle \otimes |q_1\rangle$$

$$= c_{00}|00\rangle + c_{01}|01\rangle + c_{10}|10\rangle + c_{11}|11\rangle$$

$$|q_0, q_1\rangle = |q_0\rangle \otimes |q_1\rangle = \begin{pmatrix} a_0 \\ a_1 \end{pmatrix} \otimes \begin{pmatrix} b_0 \\ b_1 \end{pmatrix}$$

$$= \begin{pmatrix} a_0 \times \begin{pmatrix} b_0 \\ b_1 \end{pmatrix} \\ a_1 \times \begin{pmatrix} b_0 \\ b_1 \end{pmatrix} \end{pmatrix} = \begin{pmatrix} a_0 b_0 \\ a_0 b_1 \\ a_1 b_0 \\ a_1 b_1 \end{pmatrix} = \begin{pmatrix} c_{00} \\ c_{01} \\ c_{10} \\ c_{11} \end{pmatrix}$$

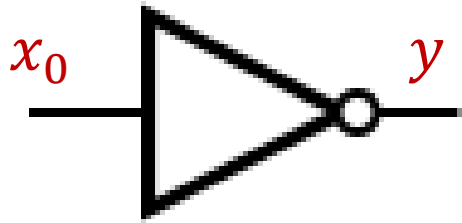
Logic Gates vs. Quantum Logic Gates

Logic function	American (MIL/ANSI) Symbol		British (BS3939) Symbol		Common German Symbol		International Electrotechnical Commission (IEC) Symbol	
	IN	OUT	IN	OUT	IN	OUT	IN	OUT
Buffer								
Inverter (NOT gate)								
2-input AND gate								
2-input NAND gate								
2-input OR gate								
2-input NOR gate								
2-input EX-OR gate								
2-input EX-NOR gate								

Operator	Gate(s)	Matrix
Pauli-X (X)		$\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$
Pauli-Y (Y)		$\begin{bmatrix} 0 & -i \\ i & 0 \end{bmatrix}$
Pauli-Z (Z)		$\begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix}$
Hadamard (H)		$\frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$
Phase (S, P)		$\begin{bmatrix} 1 & 0 \\ 0 & i \end{bmatrix}$
$\pi/8$ (T)		$\begin{bmatrix} 1 & 0 \\ 0 & e^{i\pi/4} \end{bmatrix}$
Controlled Not (CNOT, CX)		$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$
Controlled Z (CZ)		$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{bmatrix}$
SWAP		$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$
Toffoli (CCNOT, CCX, TOFF)		$\begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$

Logic Gates v.s. Quantum Logic Gates

Single-bit Gate



Not Gate

x_0	y
0	1
1	0

Single-Qubit Gates

- **Pauli operators: X, Y, Z Gates**
- Hadamard gate: H Gate
- General gate: U Gate

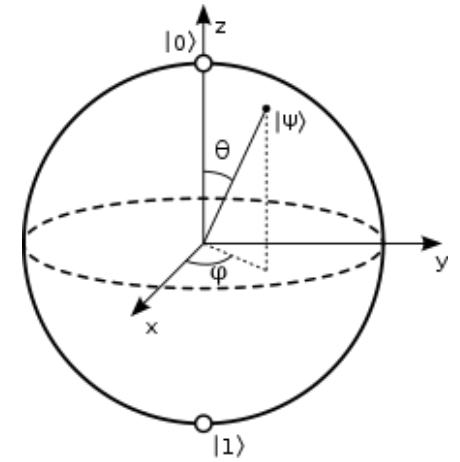
$$|0\rangle \text{ --- } \boxed{X} \text{ --- } \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$$

$$\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \times \begin{bmatrix} 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 \\ 1 \end{bmatrix}$$

$$|0\rangle \quad |1\rangle$$

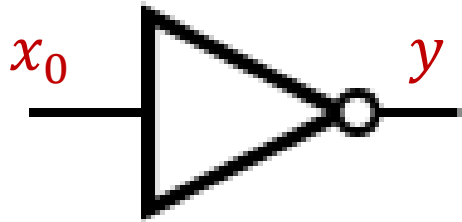
$$|1\rangle \text{ --- } \boxed{Z} \text{ --- } \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix}$$

$$\begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix} \times \begin{bmatrix} 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 0 \\ -1 \end{bmatrix}$$



Superposition

Single-bit Gate

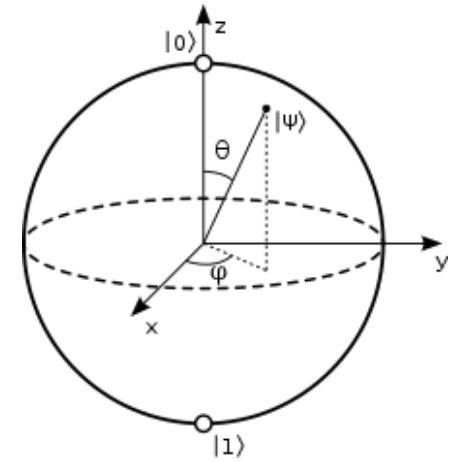


Not Gate

x_0	y
0	1
1	0

Single-Qubit Gates

- Pauli operators: X, Y, Z Gates
- **Hadamard gate: H Gate**
- **General gate: U Gate**



$$|0\rangle \text{ --- } \boxed{\text{H}} \text{ --- } \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$$

$$\frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} \times \begin{bmatrix} 1 \\ 0 \end{bmatrix} = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 \\ 1 \end{bmatrix}$$

$$|0\rangle \text{ --- } \boxed{\text{U}} \text{ --- } \begin{bmatrix} \cos(\theta/2) & -e^{i\lambda} \sin(\theta/2) \\ e^{i\phi} \sin(\theta/2) & e^{i(\phi+\lambda)} \cos(\theta/2) \end{bmatrix}$$

$$R_x(\theta) = \exp(-iX\theta/2) = \begin{bmatrix} \cos(\theta/2) & -i \sin(\theta/2) \\ -i \sin(\theta/2) & \cos(\theta/2) \end{bmatrix},$$

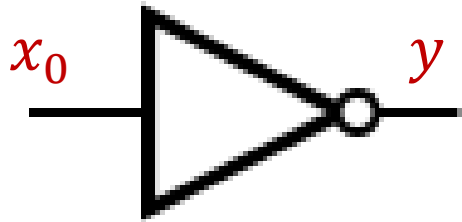
$$R_y(\theta) = \exp(-iY\theta/2) = \begin{bmatrix} \cos(\theta/2) & -\sin(\theta/2) \\ \sin(\theta/2) & \cos(\theta/2) \end{bmatrix},$$

$$R_z(\theta) = \exp(-iZ\theta/2) = \begin{bmatrix} \exp(-i\theta/2) & 0 \\ 0 & \exp(i\theta/2) \end{bmatrix}.$$



Single-Qubit Gates in Parallel

Single-bit Gate

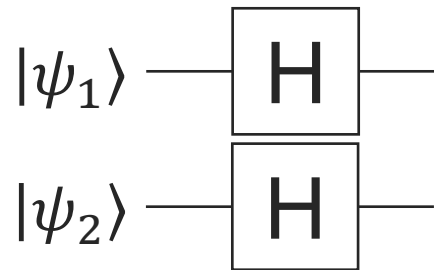


Not Gate

x_0	y
0	1
1	0

Single-Qubit Gates

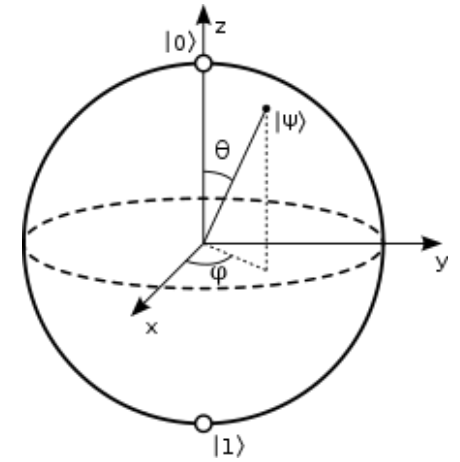
- Pauli operators: X, Y, Z Gates
- Hadamard gate: H Gate
- General gate: U Gate



$$|00\rangle = \begin{bmatrix} 1 \\ 0 \end{bmatrix} \otimes \begin{bmatrix} 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}$$

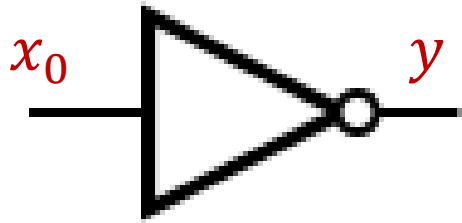
$$H^{\otimes 2} = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} \otimes \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} = \frac{1}{2} \begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & -1 & 1 & -1 \\ 1 & 1 & -1 & -1 \\ 1 & -1 & -1 & 1 \end{bmatrix}$$

$$H^{\otimes 2}|00\rangle = \frac{1}{2} \begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & -1 & 1 & -1 \\ 1 & 1 & -1 & -1 \\ 1 & -1 & -1 & 1 \end{bmatrix} \times \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} = \frac{1}{2} \begin{bmatrix} 1 \\ 1 \\ 1 \\ 1 \end{bmatrix}$$



Single-Qubit Gates in Parallel

Single-bit Gate

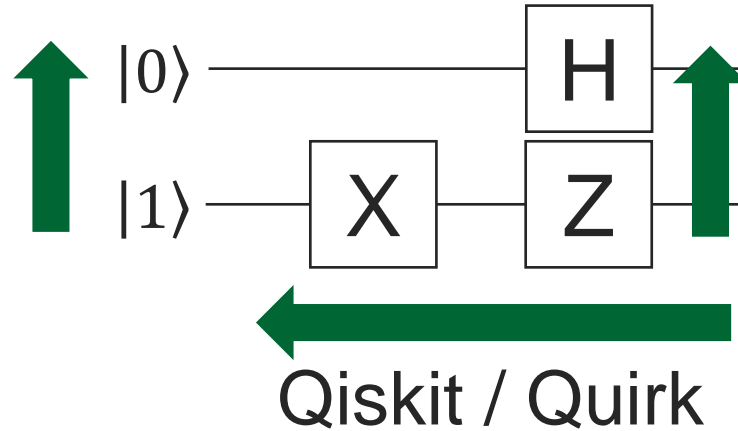


Not Gate

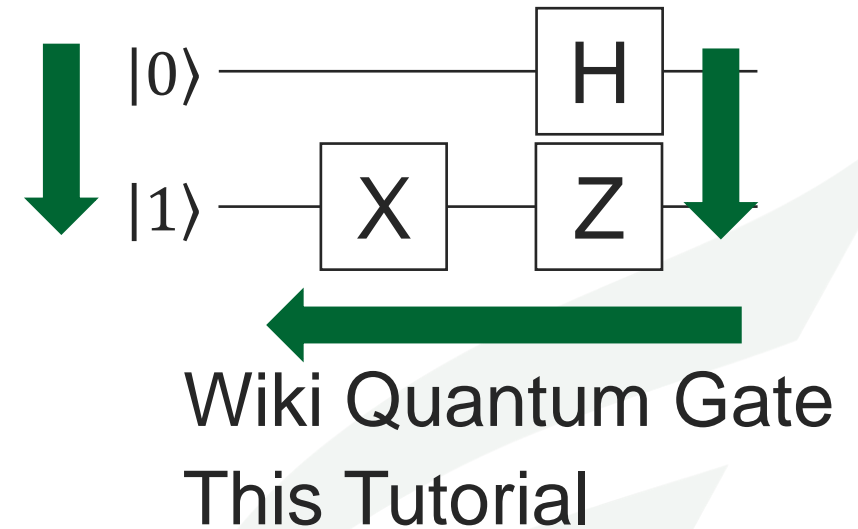
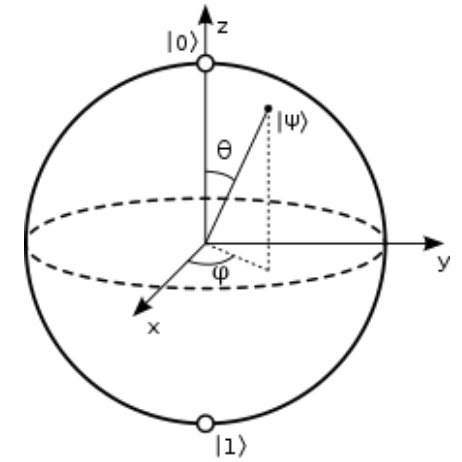
x_0	y
0	1
1	0

Single-Qubit Gates

- **Pauli operators: X, Y, Z Gates**
- Hadamard gate: H Gate
- General gate: U Gate



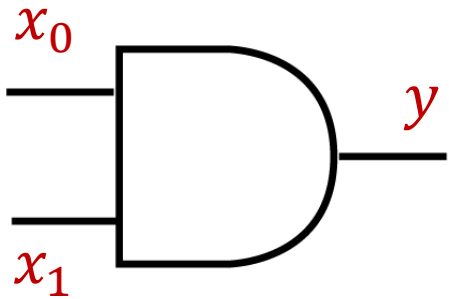
$$|\psi\rangle = (Z \otimes H) \times (X \otimes I) \times |10\rangle$$



$$|\psi\rangle = (H \otimes Z) \times (I \otimes X) \times |01\rangle$$

Logic Gates v.s. Quantum Logic Gates

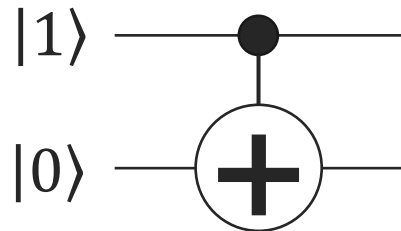
Two-bits Gate



AND Gate

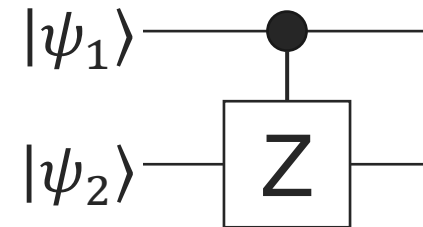
x_0	x_1	y
0	0	0
0	1	0
1	0	0
1	1	1

- Multi-Qubit Gates
 - Controlled-Pauli gates
 - Toffoli gate or CCNOT
 -



$$|10\rangle = \begin{bmatrix} 0 \\ 1 \end{bmatrix} \otimes \begin{bmatrix} 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 1 \\ 0 \end{bmatrix} \quad |11\rangle = \begin{bmatrix} 0 \\ 1 \end{bmatrix} \otimes \begin{bmatrix} 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \end{bmatrix}$$

$$CNOT \times |10\rangle = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix} \times \begin{bmatrix} 0 \\ 0 \\ 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \end{bmatrix}$$

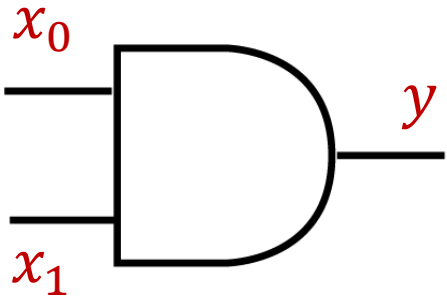


$$CZ \times |\psi_1\psi_2\rangle = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{bmatrix} \times \begin{bmatrix} a \\ b \\ c \\ d \end{bmatrix} = \begin{bmatrix} a \\ b \\ c \\ -d \end{bmatrix}$$



Entanglement

Two-bits Gate

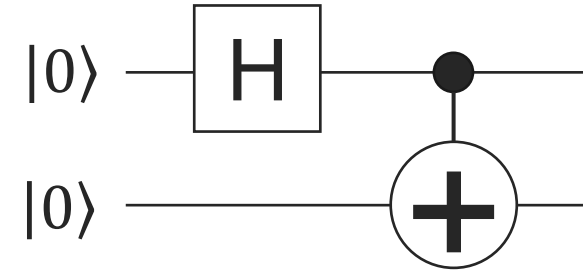
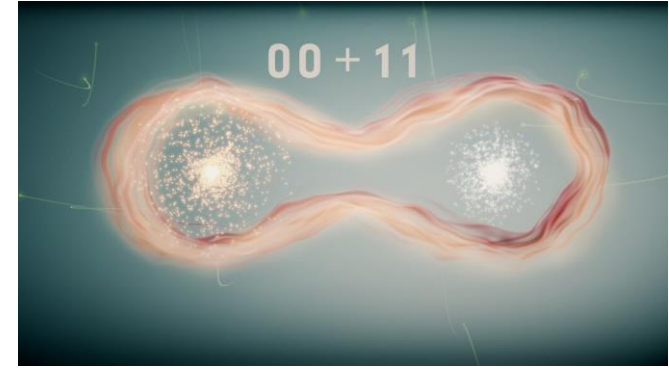
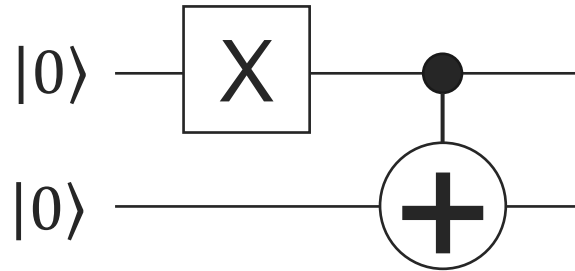


AND Gate

x_0	x_1	y
0	0	0
0	1	0
1	0	0
1	1	1

$$CNOT \times |10\rangle = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix} \times \begin{bmatrix} 0 \\ 0 \\ 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \end{bmatrix} = |1\rangle \otimes |1\rangle$$

- Multi-Qubit Gates
 - Controlled-Pauli gates
 - Toffoli gate or CCNOT
 -



$$CNOT \times (H \otimes I) \times |00\rangle = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix} \times \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 1 \\ 1 & 0 & -1 & 0 \\ 0 & 1 & 0 & -1 \end{bmatrix}$$

$$\times |00\rangle = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 1 \\ 0 & 1 & 0 & -1 \\ 1 & 0 & -1 & 0 \end{bmatrix} \times \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 \\ 0 \\ 0 \\ 1 \end{bmatrix} \begin{matrix} |00\rangle \\ |01\rangle \\ |10\rangle \\ |11\rangle \end{matrix}$$



Hands-On Tutorial

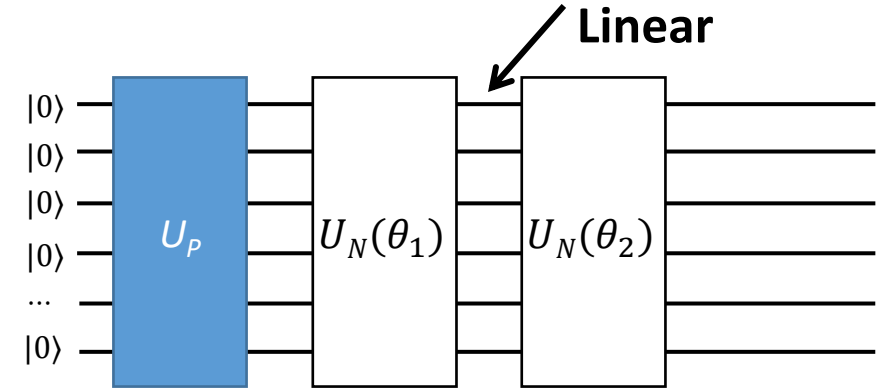
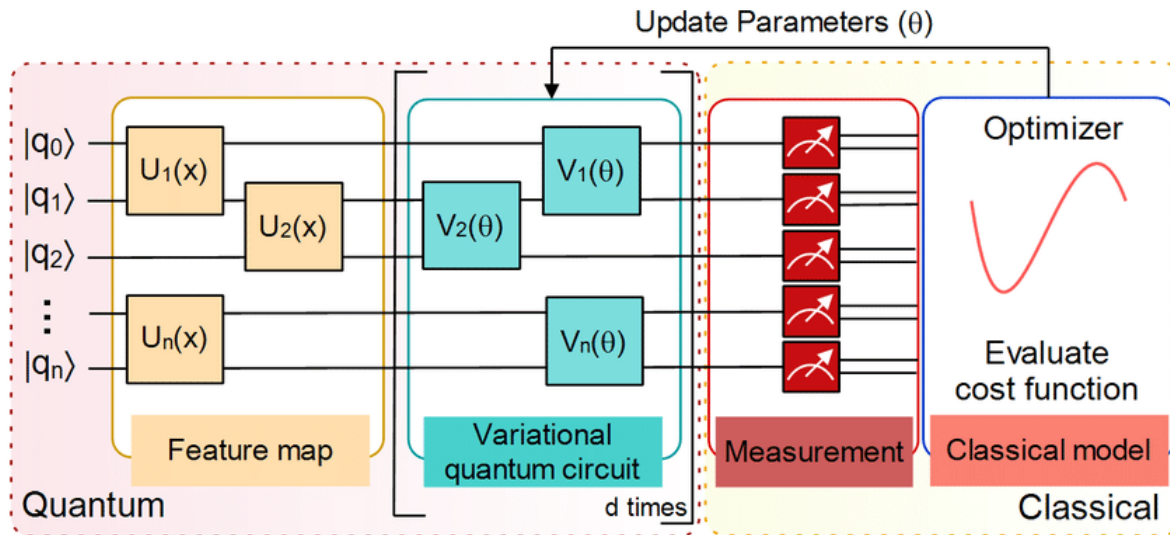
Basic Quantum Gates



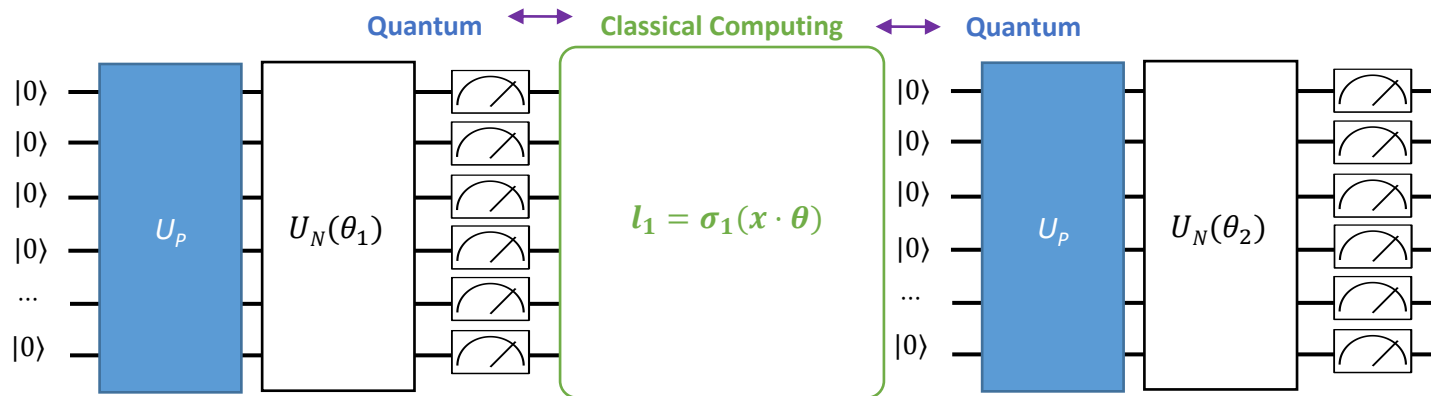
<https://jqub.ece.gmu.edu/categories/QFV/>



Two Paths of Quantum Machine Learning: Path 1 --- VQC



[ref] Sen, P., Bhatia, A.S., Bhangu, K.S. and Elbeltagi, A., 2022. Variational quantum classifiers through the lens of the Hessian. Plos one, 17(1), p.e0262346.



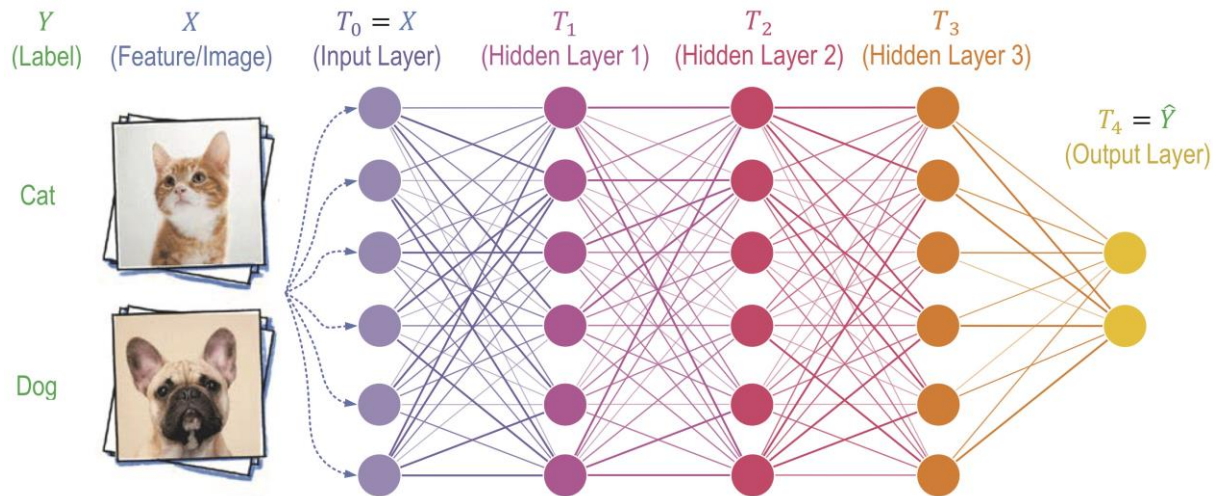
Pros:

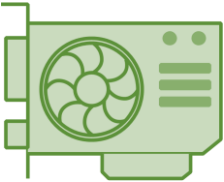
- Easy to implement

Cons:

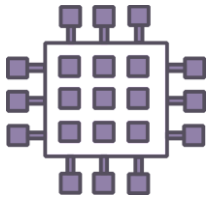
- On intermediate-scale quantum devices, no works show that **performance** of QML can beat classical ML, so far.
- Have no **non-linear** in the network
- Incur **heavy overhead** for **non-linearity**

Two Paths of Quantum Machine Learning: Path 2 --- Q Accelerator





CPU/GPU Accelerator



FPGA/ASIC Accelerator



Quantum Accelerator

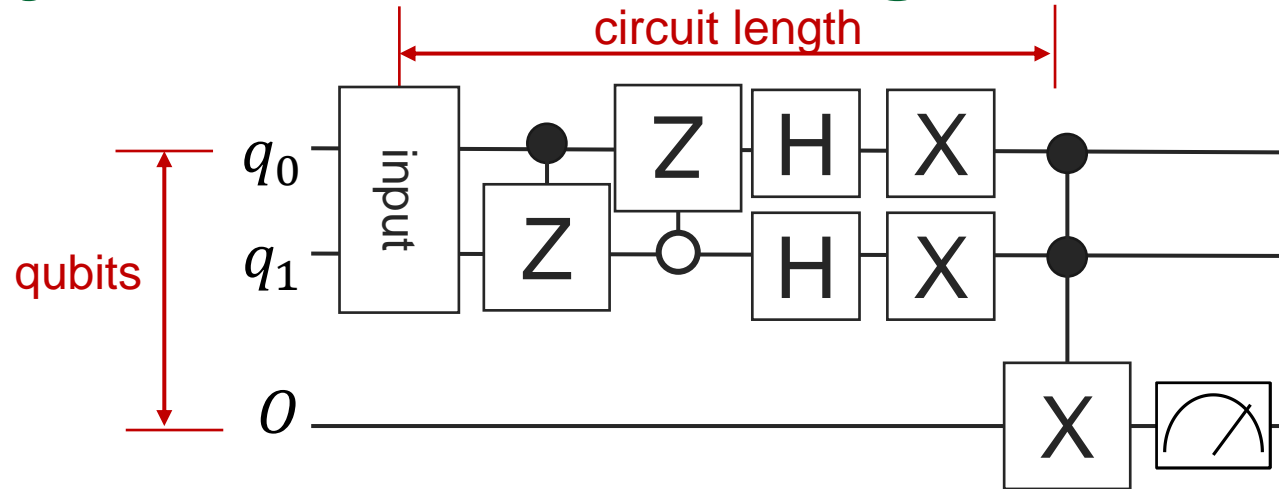
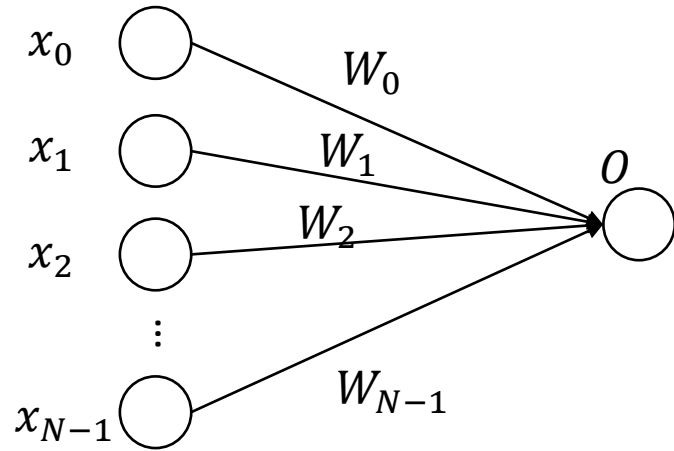
Pros:

- Same Performance as Classical ML

Questions:

- How to design?
- Advantage?

What's the complexity? Quantum Advantage?



- **Classical computer with 1 MAC**

Time: $O(N)$

Space (Comp. Res.): $O(1)$

Time \times Space: $O(N)$

- **Classical computer with N MAC**

Time: $O(1)$

Space (Comp. Res.): $O(N)$

Time \times Space: $O(N)$

- **Time-Space Complexity in Quantum computer**

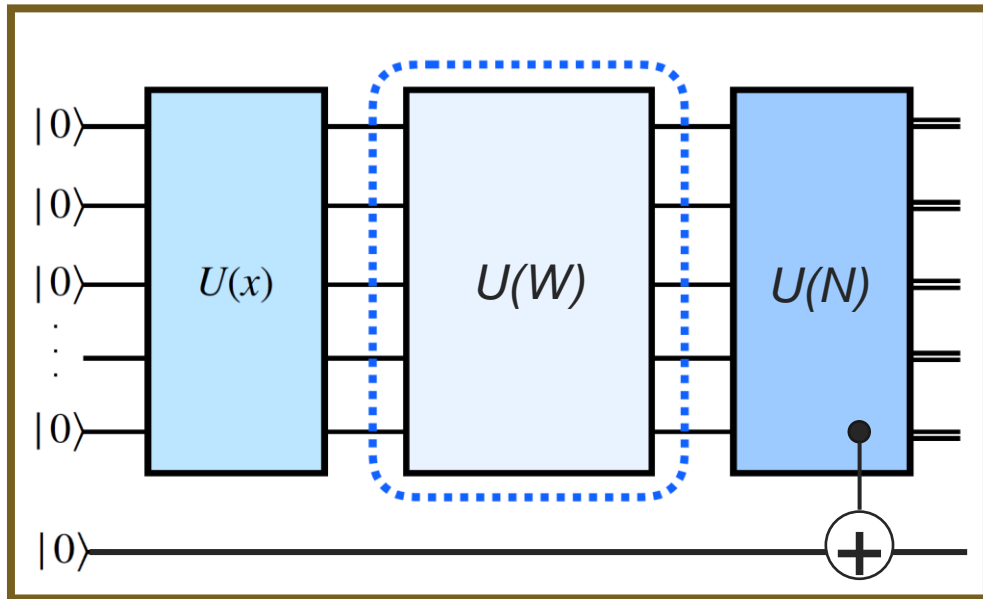
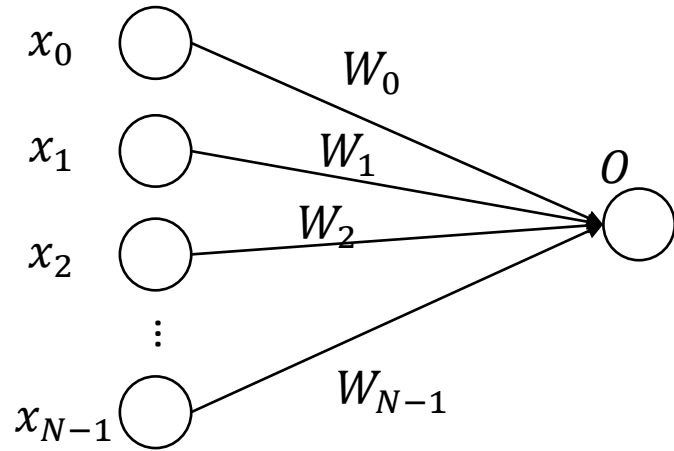
Time: Circuit Length

Space (Comp. Res.): Qubits

Time \times Space ($T - S$): Qubits \times Circuit Length

- **Given that $T - S$ complexity on classical computer is $O(N)$, Quantum Advantage is achieved if $T - S$ complexity on Quantum can be $O(\text{polylog}N)$ or lower. ----- Exponential Speedup!**

What's the Goals?



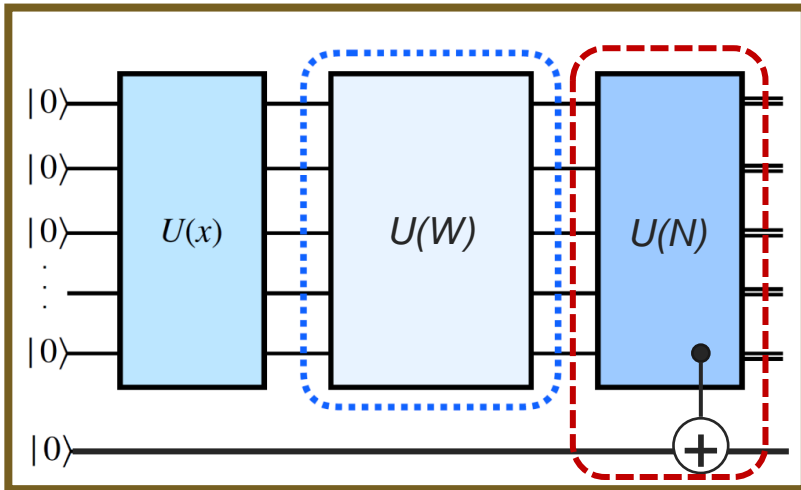
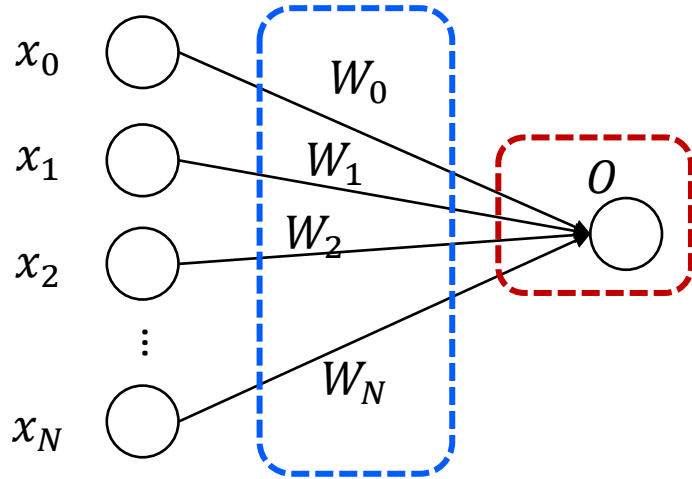
Input features	Number of Qubits	Number of Gates
$U(x)$	$O(\log N)$	$O(?)$
$U(W)$	$O(\log N)$	$O(?)$
$U(N)$	$O(1)$	$O(\log N)$

n: input data number

Potential Quantum Advantage

What's the Goals?

Goal 1: **Correctly** Implement!



Goal 2: **Efficiently** Implement!

$$O = \delta \left(\sum_{i \in [0, N]} x_i \times W_i \right)$$

where δ is a quadratic function

Classical Computing:

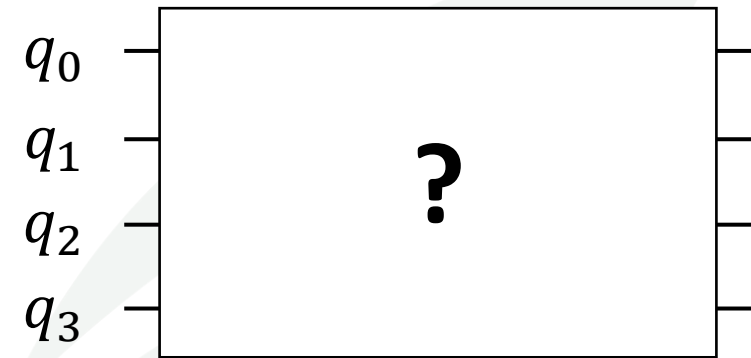
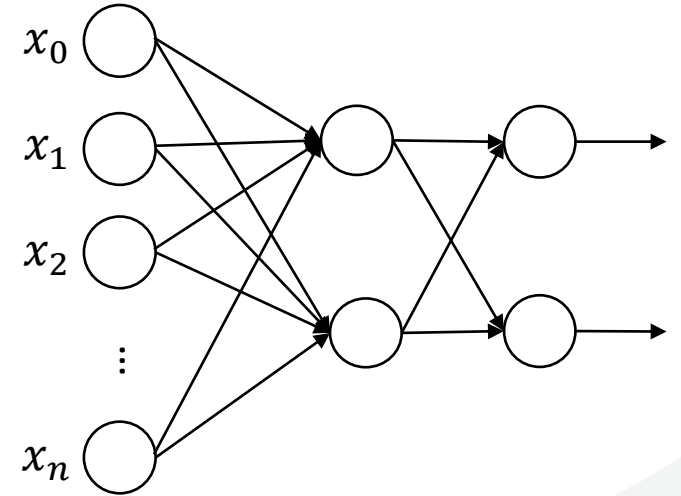
Complexity of **$O(N)$**

Quantum Computing:

Can we reduce complexity to

$O(\text{polylog} N)$, say **$O(\log^2 n)$** ?

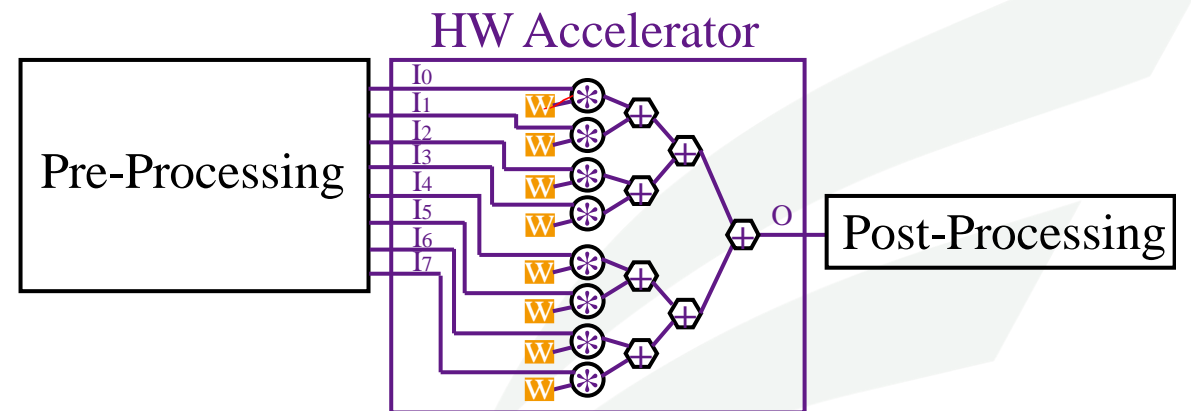
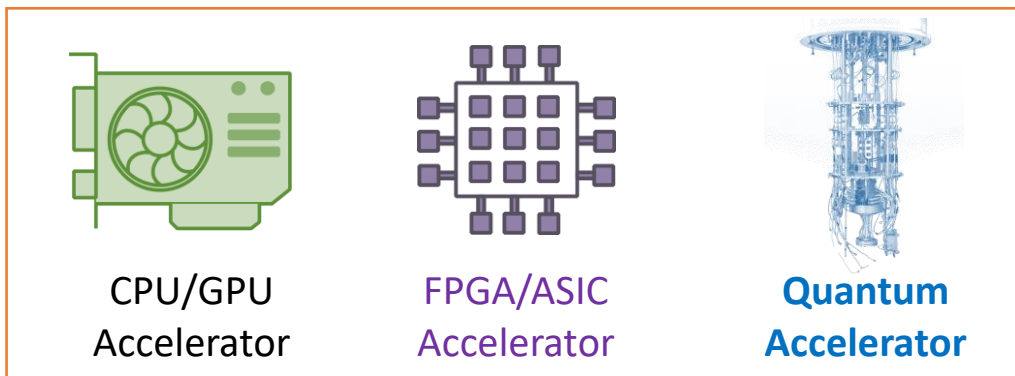
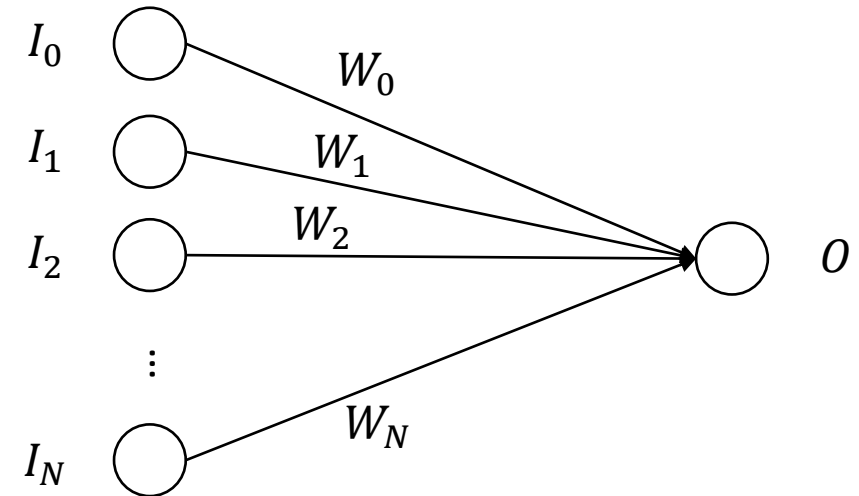
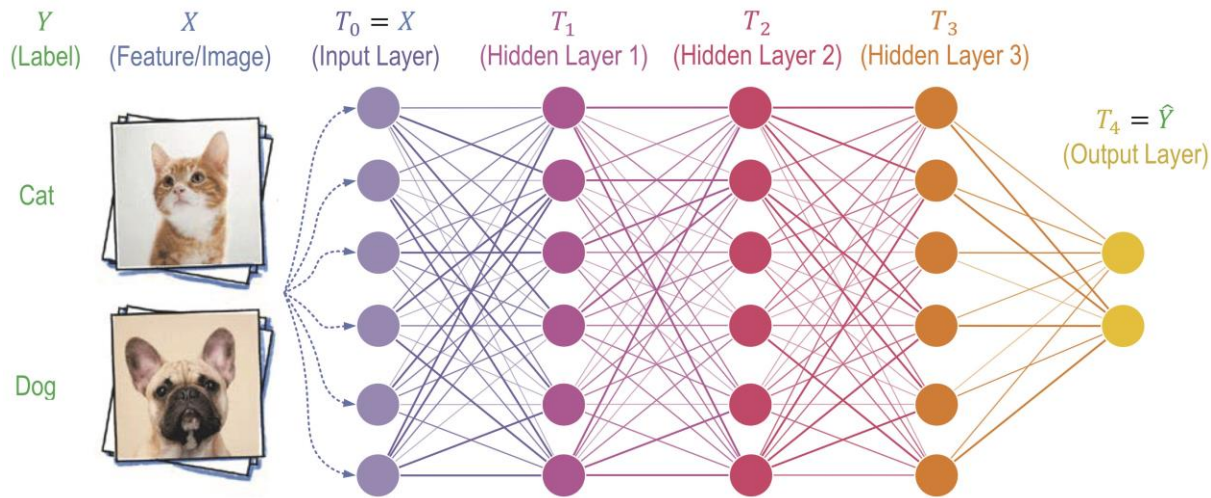
Goal 3: **Scale-Up!**



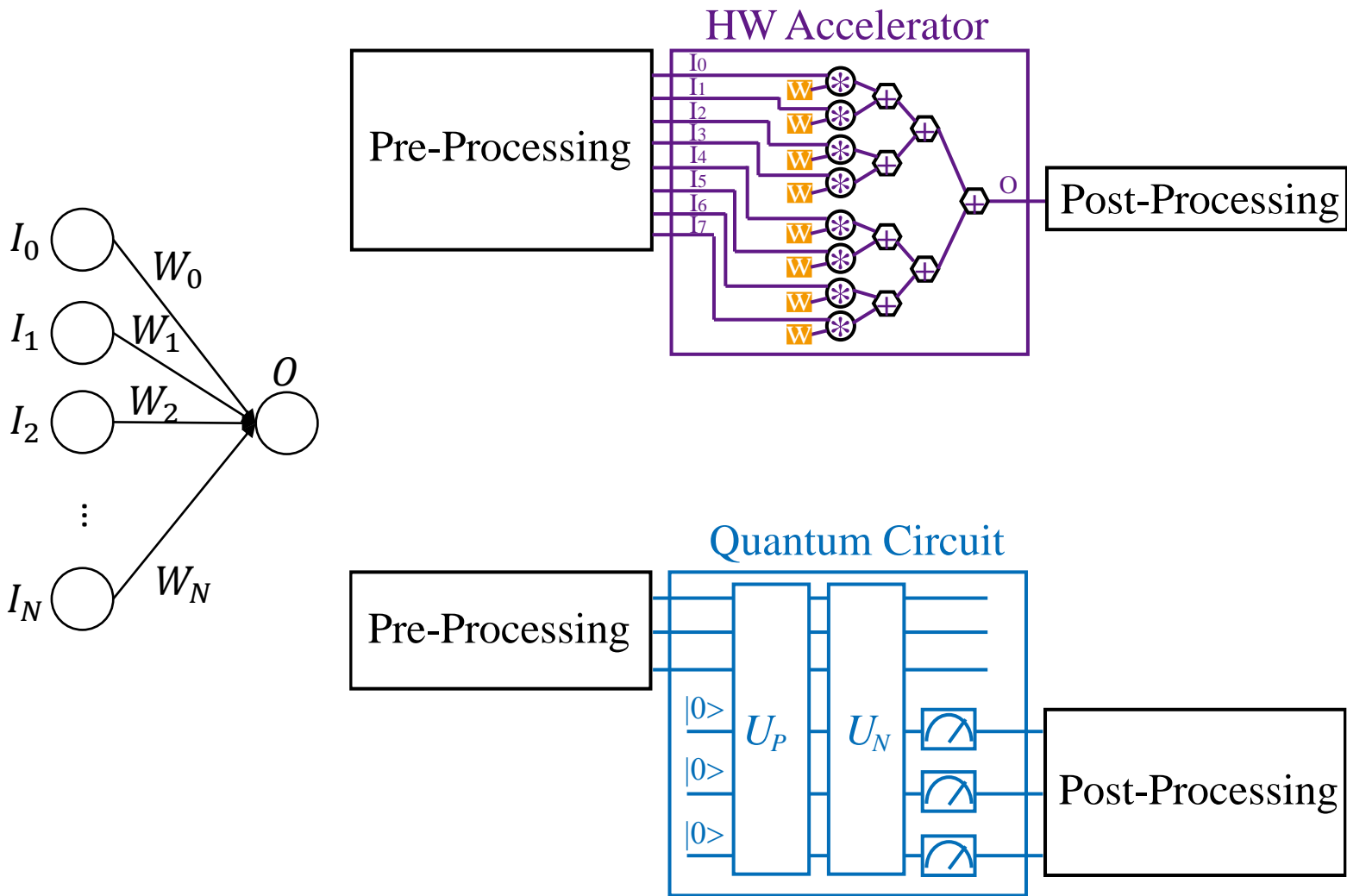
Agenda – Session 2: QuantumFlow

- **General Framework for Quantum-Based Neural Network Accelerator**
 - Data Preparation and Encoding
 - *Colab Hands-On (1): From Classical Data to Quantum Data*
 - Quantum Circuit Design
 - *Colab Hands-On (2): A Quantum Neuron*
- **Co-Design toward Quantum Advantage**
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 - Results

Neural Network Accelerator Design on Classical Hardware



Neural Network Accelerator Design from Classical to Quantum Computing



- (1) Data Pre-Processing (*PreP*)
- (2) HW Acceleration
- (3) Data Post-Processing (*PostP*)

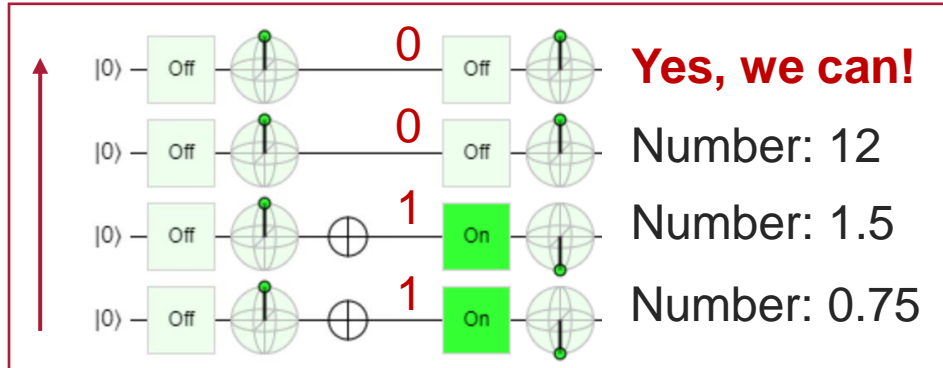
- (1) Data Pre-Processing (*PreP*)
- (2) HW/Quantum Acceleration
 - (2.1) U_p Quantum-State-Preparation
 - (2.2) U_N Quantum Neural Computation
 - (2.3) M Measurement
- (3) Data Post-Processing (*PostP*)

$$PreP + U_p + U_N + M + PostP$$

What Data Can Be Encoded to Quantum Computers, and how?

- Can we encode an arbitrary number into quantum computer? Is it efficient?

▪ **Yes / No**



No, because it uses too many qubits!

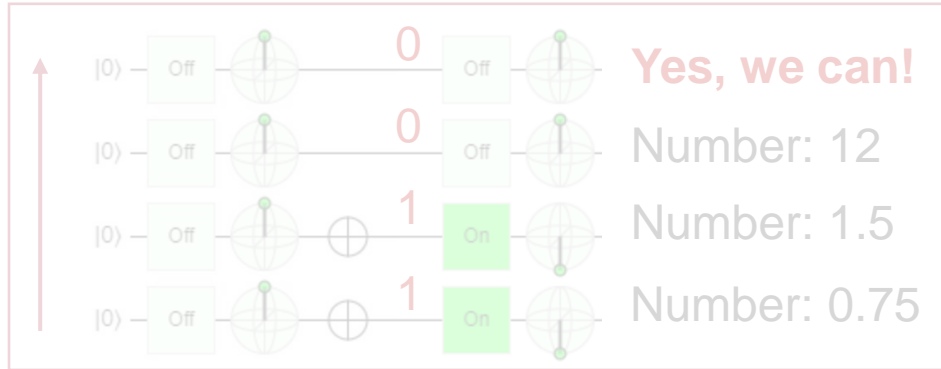
This encoding is similar to classical bits, where each qubit is regarded as a binary number!

1-to-N mapping! (Boolean Function)

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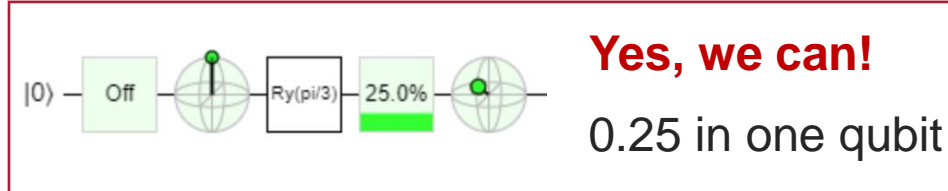
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- Can we take use of superposition of qubits to encode data? Is this solution perfect?

- Yes / No



No, (1) data needs in the range of $[0,1]$!

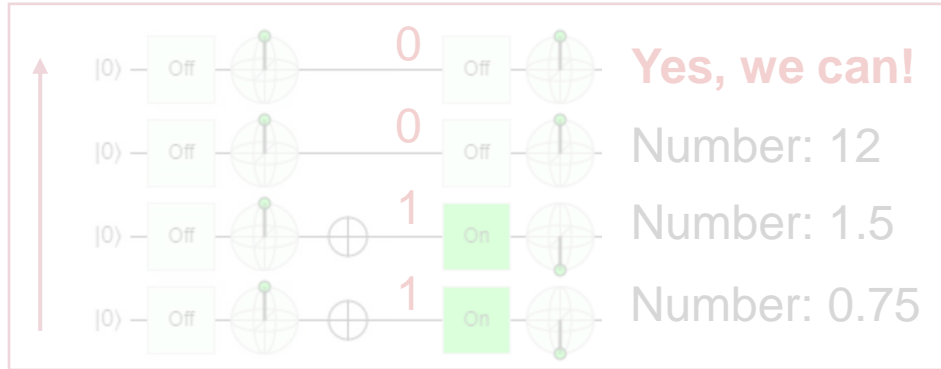
(2) same complexity $O(1)$ as classical

1-to-1 mapping! (Angle Encoding)

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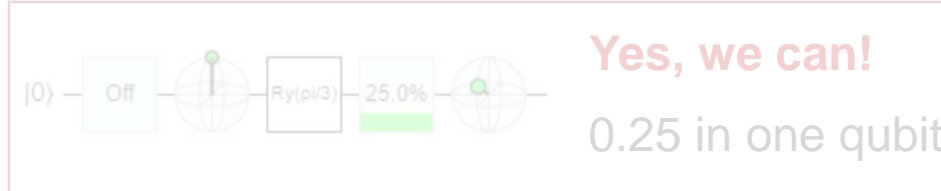
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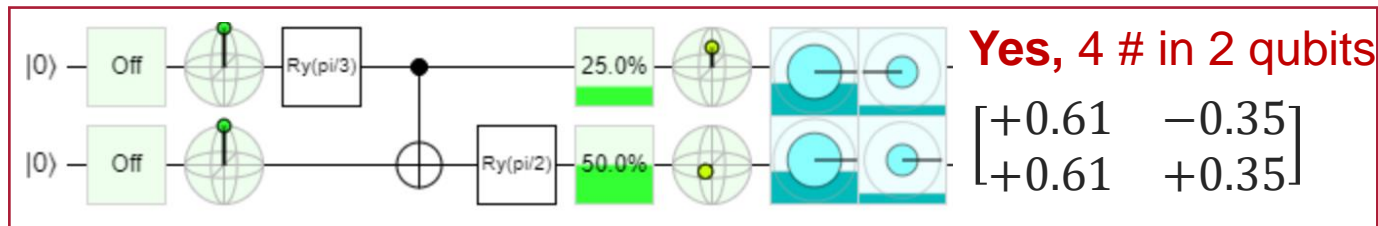
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1-to-1 mapping! (Angle Encoding)

- Can we take use of entanglement of qubits to encode data? Is this solution perfect?

- **Yes / No**



**No, (1) sum of the square of data need to be 1
 (2) may have high cost to encode data**

N-to-logN mapping! (Amplitude Encoding)

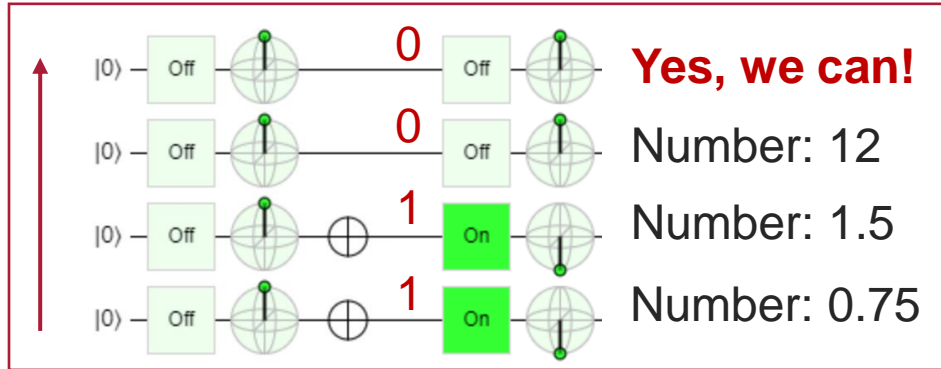
Encoding: 1-to-N v.s. 1-to-1 v.s. N-to-logN

Data Encoding	# of Qubit (C v.s. Q)	Data Limitation	Encoding Complexity
1-to-N	$O(N)$ v.s. $O(pN)$	Almost No!	Low
1-to-1	$O(N)$ v.s. $O(N)$	$[0,+1]$	Low
N-to-logN	$O(N)$ v.s. $O(\log N)$	$[-1,+1]$ and $\sum x^2 = 1$	High

What Data Can Be Encoded to Quantum Computers, and how?

- Can we encode an arbitrary number into quantum computer? Is it efficient?

▪ **Yes / No**



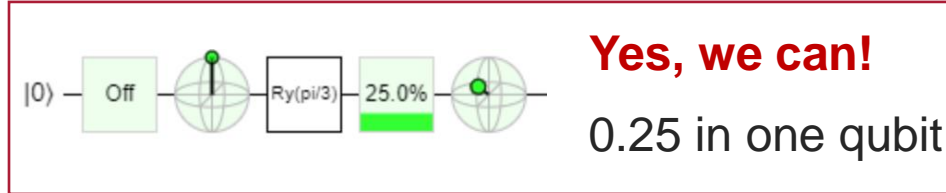
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This encoding is similar to classical bits, where each qubit is regarded as a binary number!

1-to-N mapping! (Boolean Function)

- Can we take use of superposition of qubits to encode data? Is this solution perfect?

▪ **Yes / No**



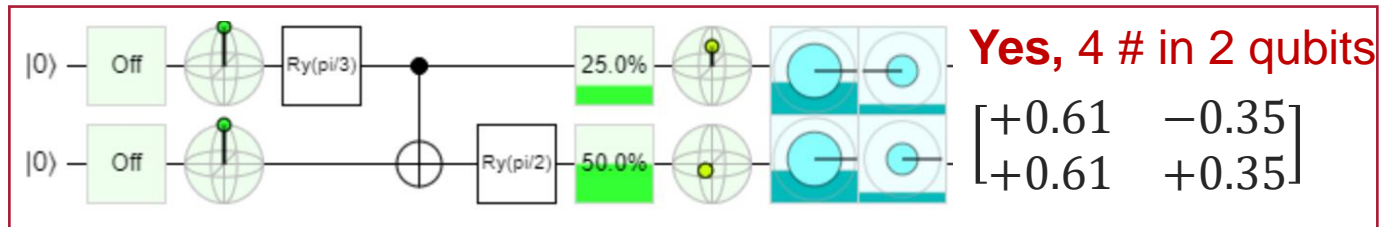
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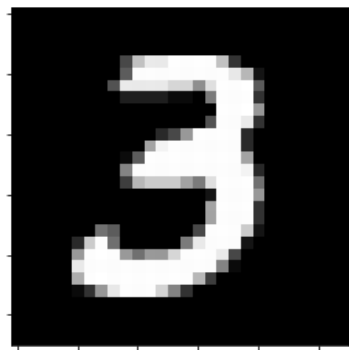
Hands-On: QuantumFlow

A Co-Design Framework of Neural Networks and Quantum Circuits Towards Quantum Advantage

Published at Nature Communications 2021

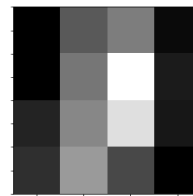
PreP + U_P + U_N + M + *PostP*: Data Pre-Processing

- **Given:** (1) 28×28 image, (2) the number of qubits to encode data (say $Q=4$ qubits in the example)
- **Do:** (1) downsampling from 28×28 to $2^Q = 16 = 4 \times 4$; (2) converting data to be the state vector in a unitary matrix
- **Output:** A unitary matrix, $M_{16 \times 16}$



Step 1: Downsampling

From 28×28 to 4×4



$$\begin{bmatrix} 0.0039 & 0.2118 & 0.2941 & 0.0275 \\ 0.0039 & 0.2784 & 0.5961 & 0.0667 \\ 0.0863 & 0.3176 & 0.5216 & 0.0588 \\ 0.1137 & 0.3608 & 0.1725 & 0.0039 \end{bmatrix}$$

$$\begin{bmatrix} 0.0039 & 0.2118 & 0.2941 & 0.0275 \\ 0.0039 & 0.2784 & 0.5961 & 0.0667 \\ 0.0863 & 0.3176 & 0.5216 & 0.0588 \\ 0.1137 & 0.3608 & 0.1725 & 0.0039 \end{bmatrix}$$

Step 2: Formulate Unitary Matrix

**Applying SVD method
(See Listing 1 in ASP-DAC SS Paper)**

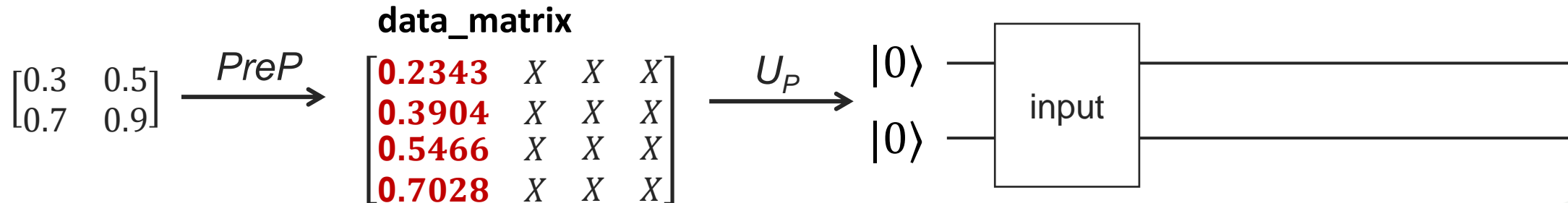
Unitary matrix: $M_{16 \times 16}$

[SS] W. Jiang, et al. [When Machine Learning Meets Quantum Computers: A Case Study](#), ASP-DAC'21

PreP + U_P + U_N + M + *PostP* --- Data Encoding / Quantum State Preparation

- **Given:** The unitary matrix provided by *PreP*, $M_{16 \times 16}$
- **Do:** Quantum-State-Preparation, encoding data to qubits
- **Verification:** Check the amplitude of states are consistent with the data in the unitary matrix, $M_{16 \times 16}$

Let's use a 2-qubit system as an example to encode a matrix $M_{4 \times 4}$



State Transition:

$$\begin{bmatrix} \mathbf{0.2343} & X & X & X \\ \mathbf{0.3904} & X & X & X \\ \mathbf{0.5466} & X & X & X \\ \mathbf{0.7028} & X & X & X \end{bmatrix} \times \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} = \begin{bmatrix} \mathbf{0.2343} \\ \mathbf{0.3904} \\ \mathbf{0.5466} \\ \mathbf{0.7028} \end{bmatrix}$$

IBM Qiskit Implementation:

```
inp = QuantumRegister(4, "in_qubit")
circ = QuantumCircuit(inp)
iniG = UnitaryGate(data_matrix, label="input")
circ.append(iniG, inp[0:4])
```

Hands-On Tutorial (1)

PreP + U_p

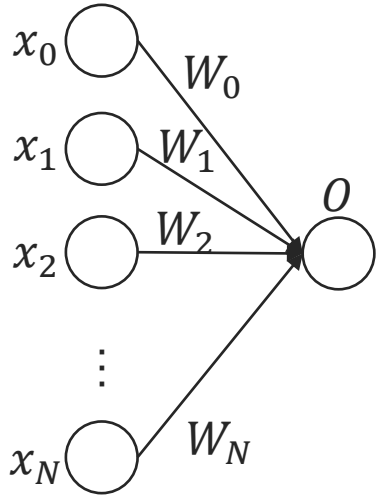


<https://jqub.ece.gmu.edu/categories/QFV/>

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PreP + U_P + U_N + M + PostP --- Neural Computation



- **Given:** (1) A circuit with encoded input data x ; (2) the trained binary weights w for one neural computation, which will be associated to each data.
- **Do:** Place quantum gates on the qubits, such that it performs $\frac{(x*w)^2}{\|x\|}$.
- **Verification:** Whether the output data of quantum circuit and the output computed using torch on classical computer are the same.

$$\text{Target: } O = \left[\frac{\sum_i (x_i \times w_i)}{\sqrt{\|x\|}} \right]^2$$

- **Assumption 1:** Parameters/weights (W_0 --- W_N) are binary weight, either +1 or -1
- **Assumption 2:** The weight $W_0 = +1$, otherwise we can use $-w$ (quadratic func.)

$$\text{Step 1: } m_i = x_i \times w_i$$

$$\text{Step 2: } n = \left[\frac{\sum_i (m_i)}{\sqrt{\|x\|}} \right]$$

$$\text{Step 3: } O = n^2$$

Quantum Neuron Design: Step 1

Step 1: $m_i = x_i \times w_i$

EX: 4 input data on 2 qubits

$$\mathbf{x} = \begin{bmatrix} a_0 \\ a_1 \\ a_2 \\ a_3 \end{bmatrix} \quad \mathbf{w} = \begin{bmatrix} w_0 \\ w_1 \\ w_2 \\ w_3 \end{bmatrix} \quad \begin{matrix} w_0 = 1 \\ w_1 = 1 \\ w_2 = 1 \\ w_3 = -1 \end{matrix} \quad \longrightarrow \quad m_3 = -1 \times a_3 = -a_3$$

Output

=

U

×

Input

a_0	$ 00\rangle$
a_1	$ 01\rangle$
a_2	$ 10\rangle$
$m_3 = -a_3$	$ 11\rangle$

=

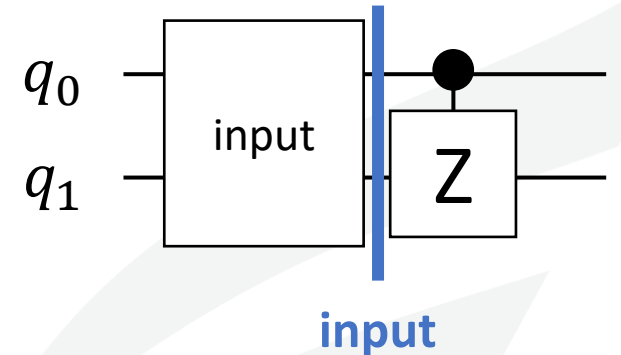
$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{bmatrix}$$

×

a_0	$ 00\rangle$
a_1	$ 01\rangle$
a_2	$ 10\rangle$
a_3	$ 11\rangle$



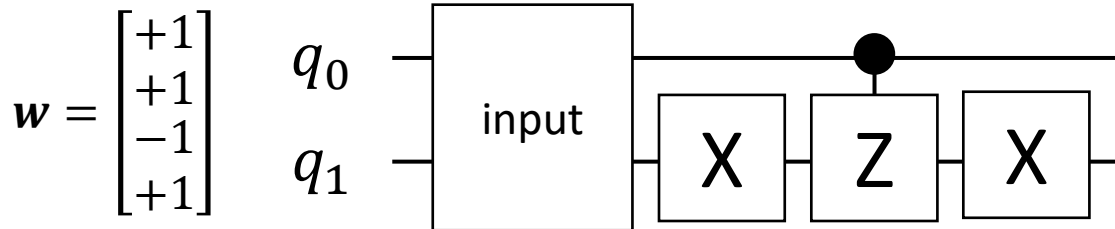
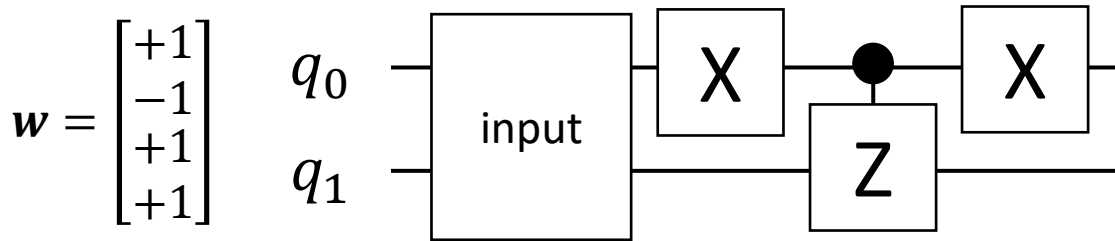
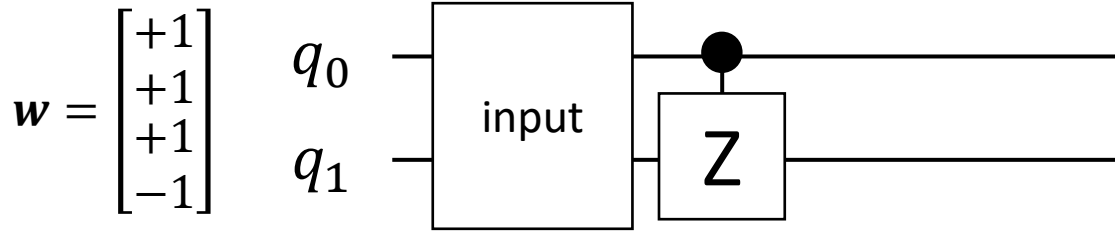
Quantum Circuit



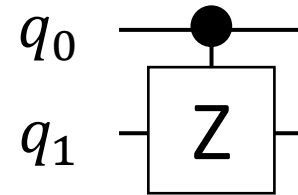
PreP + U_P + U_N + M + PostP --- Neural Computation: Step 1

Step 1: $m_i = x_i \times w_i$

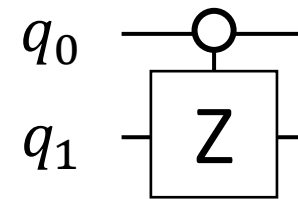
EX: 4 input data on 2 qubits



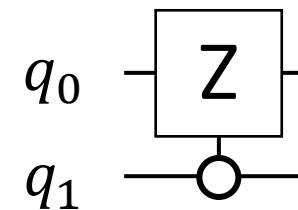
$$w = \begin{bmatrix} +1 \\ +1 \\ +1 \\ -1 \end{bmatrix} \text{ or } \begin{bmatrix} +1 \\ +1 \\ -1 \\ -1 \end{bmatrix} \text{ or } \begin{bmatrix} +1 \\ -1 \\ -1 \\ -1 \end{bmatrix} \text{ or } \begin{bmatrix} +1 \\ +1 \\ -1 \\ +1 \end{bmatrix} \text{ or } \begin{bmatrix} +1 \\ -1 \\ -1 \\ +1 \end{bmatrix} \text{ or } \begin{bmatrix} +1 \\ -1 \\ +1 \\ +1 \end{bmatrix}$$



Flip the sign of $|11\rangle$



Flip the sign of $|01\rangle$



Flip the sign of $|10\rangle$

Quantum Neuron Design: Step 2

Step 2: $n = \left\lceil \frac{\sum_i(m_i)}{\sqrt{\|x\|}} \right\rceil$

EX: 4 input data on 2 qubits

Output

$\sum_i(m_i)/\sqrt{\ x\ }$	$ 00\rangle$
Do not care 1	$ 01\rangle$
Do not care 2	$ 10\rangle$
Do not care 3	$ 11\rangle$

=

U

×

Input

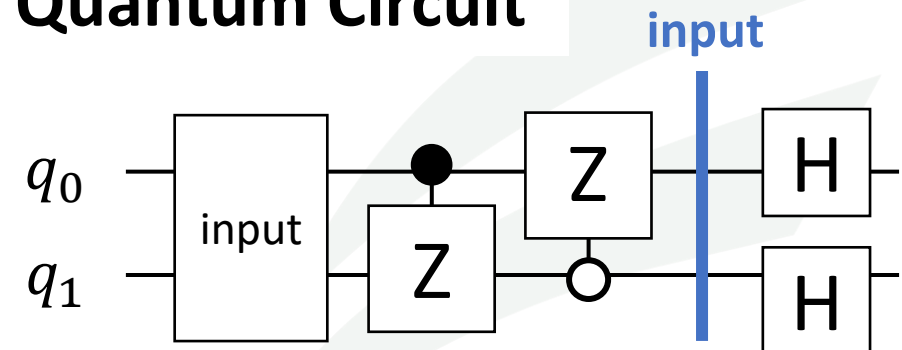
= $\frac{1}{\sqrt{\|x\|}} \begin{bmatrix} 1 & 1 & 1 & 1 \\ * & * & * & * \\ * & * & * & * \\ * & * & * & * \end{bmatrix} \times$

m_0	$ 00\rangle$
m_1	$ 01\rangle$
m_2	$ 10\rangle$
m_3	$ 11\rangle$



note: $\|x\| = 2^N$

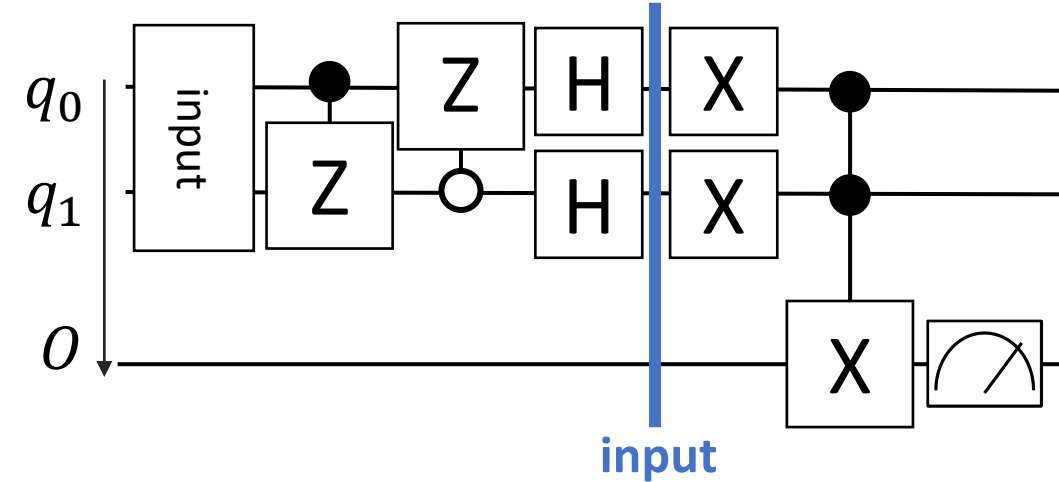
Quantum Circuit



Quantum Neuron Design: Step 3

Step 3: $O = n^2$

EX: 4 input data on 2 qubits



Input

$\sum_i (m_i) / \sqrt{\ x\ }$	$ 000\rangle$
0	$ 001\rangle$
Do not care 1	$ 010\rangle$
0	$ 011\rangle$
Do not care 2	$ 100\rangle$
0	$ 101\rangle$
Do not care 3	$ 110\rangle$
0	$ 111\rangle$

$X^{\otimes 2}$

Do not care 3	$ 000\rangle$
0	$ 001\rangle$
Do not care 2	$ 010\rangle$
0	$ 011\rangle$
Do not care 1	$ 100\rangle$
0	$ 101\rangle$
$\sum_i (m_i) / \sqrt{\ x\ }$	$ 110\rangle$
0	$ 111\rangle$

CCX

Do not care	$ 000\rangle$
0	$ 001\rangle$
Do not care	$ 010\rangle$
0	$ 011\rangle$
Do not care	$ 100\rangle$
0	$ 101\rangle$
0	$ 110\rangle$
$\sum_i (m_i) / \sqrt{\ x\ }$	$ 111\rangle$

Output

$$P\{O = |1\rangle\} = P\{|001\rangle\} + P\{|011\rangle\} + P\{|101\rangle\} + P\{|111\rangle\} = \left[\frac{\sum_i (m_i)}{\sqrt{\|x\|}} \right]^2$$

Hands-On Tutorial (2)

PreP + U_p + U_N

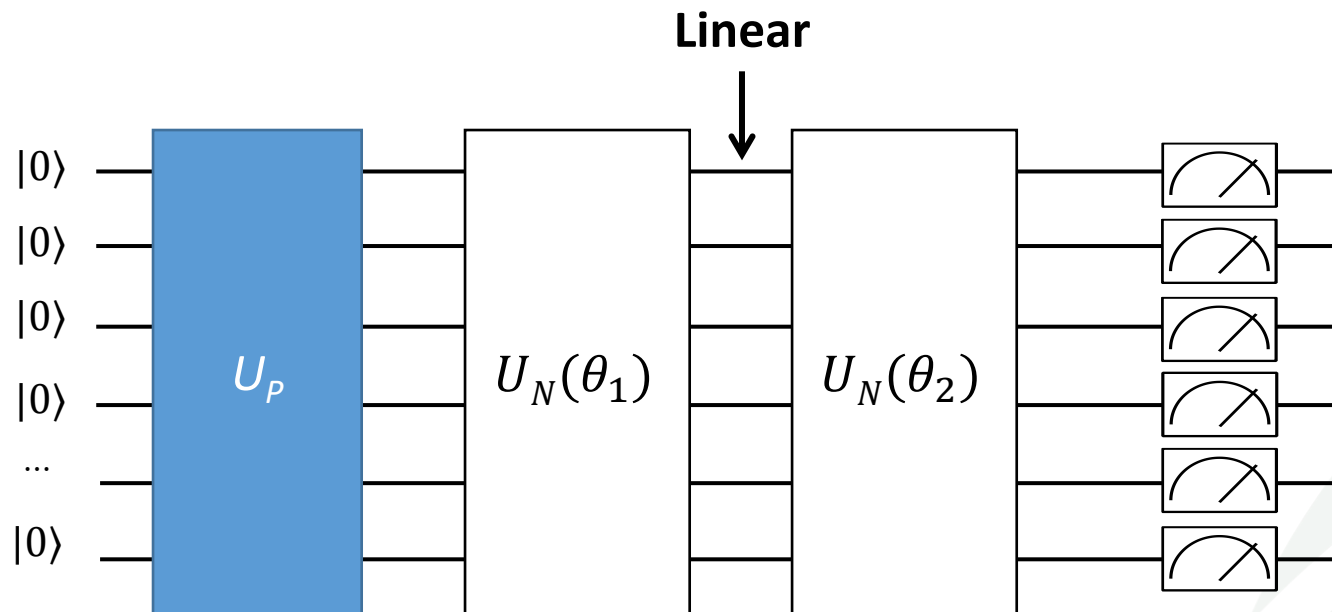
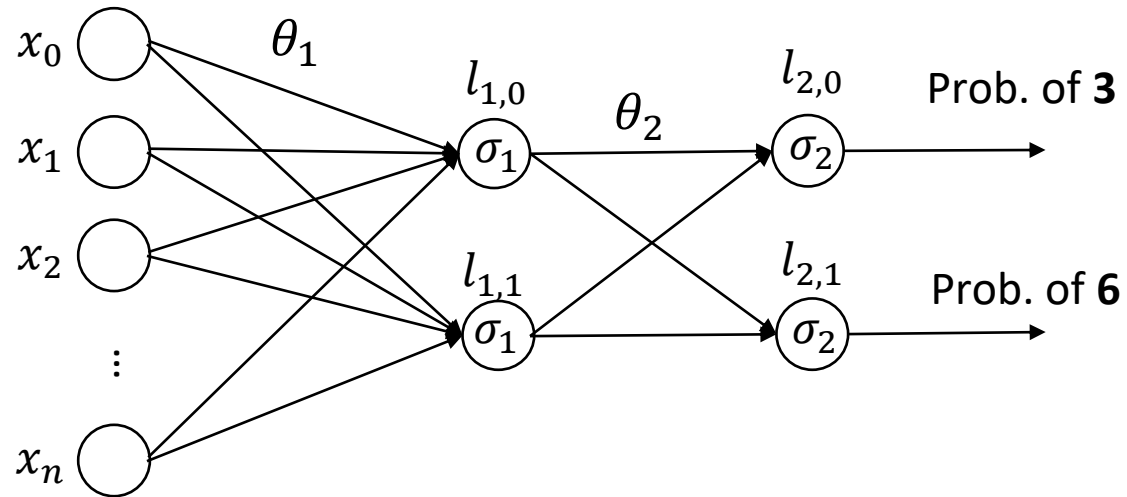


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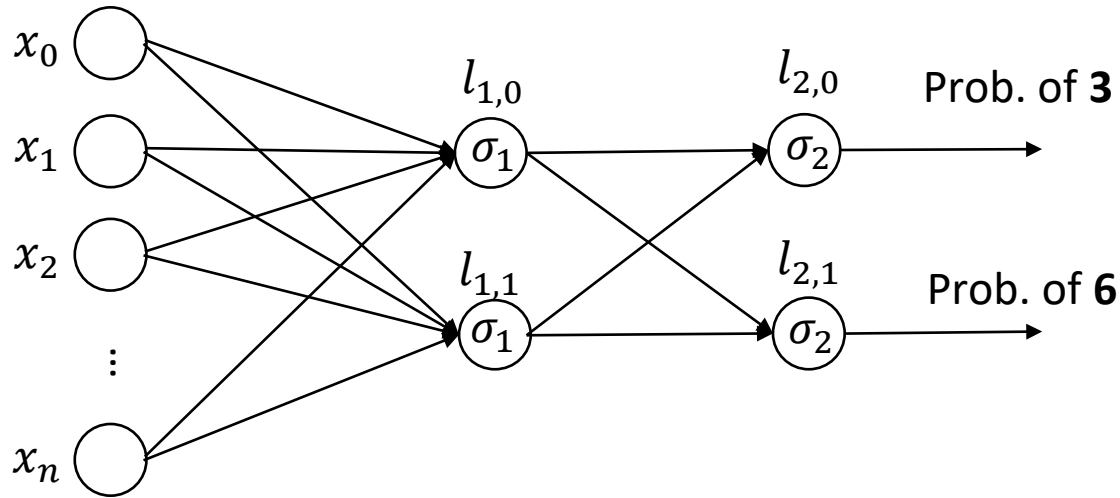
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Challenge 1: Non-linearity is Needed, But Difficult in Quantum Circuit



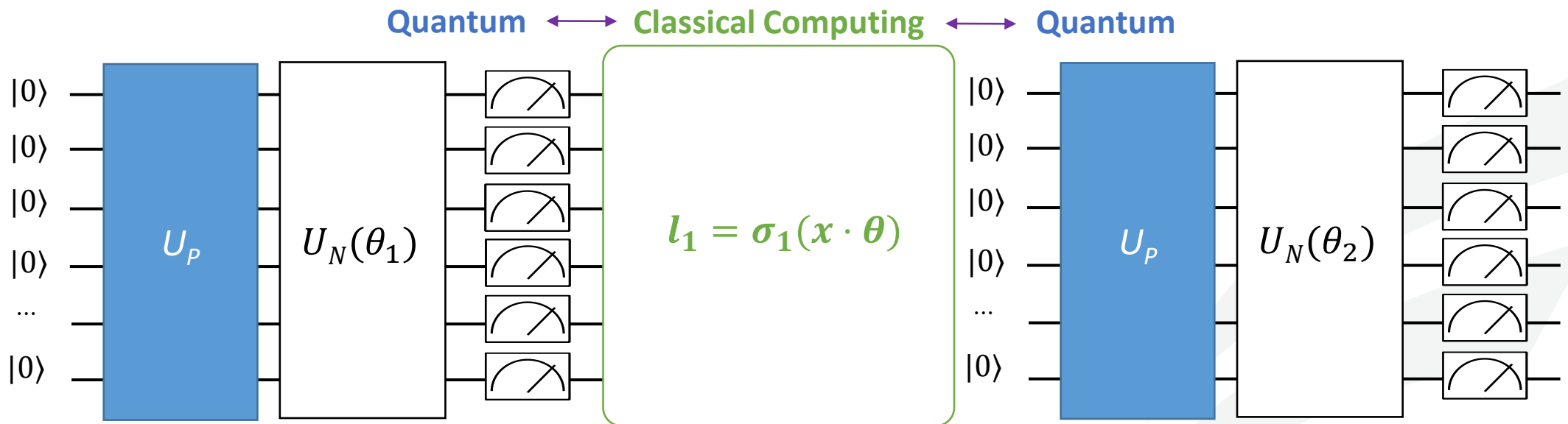
Challenge 2: Quantum-Classical Interface is Expensive



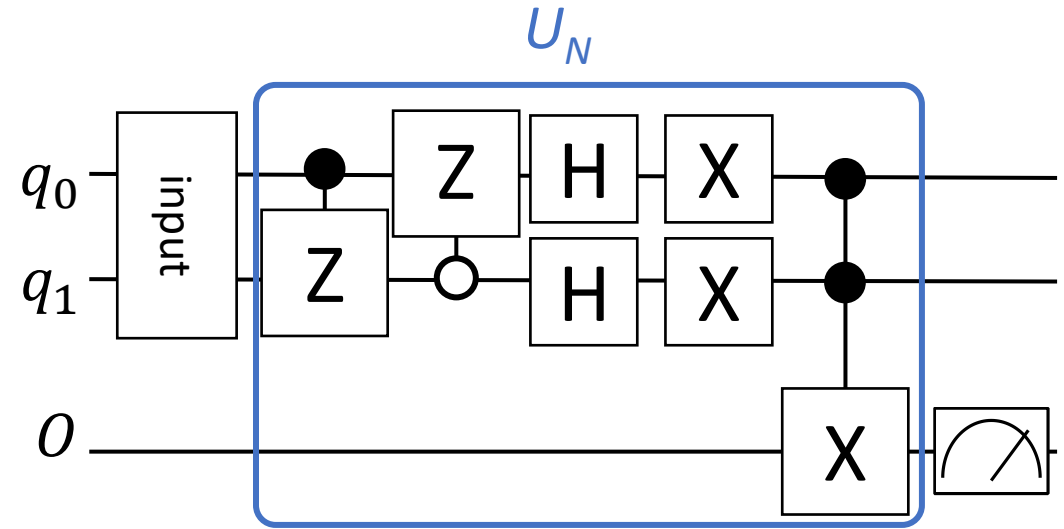
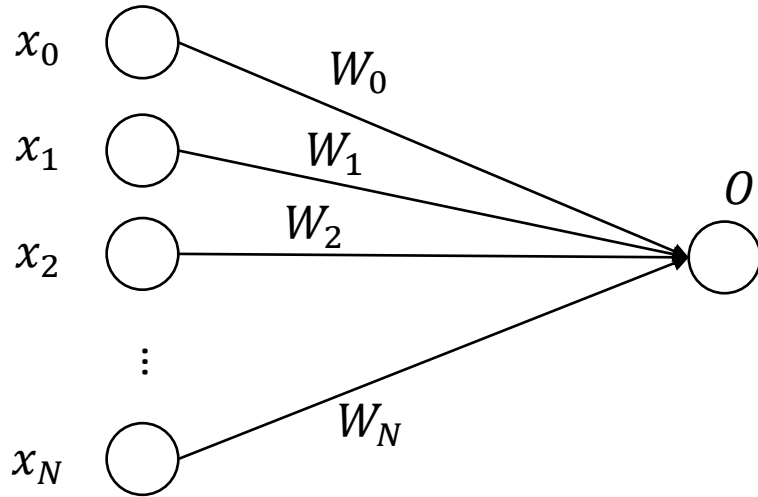
Ref [1]

Table 2 Complexity of each step in hybrid quantum-classical computing for deep neural network with U-LYR.

Complexity	State-preparation	Computation	Measurement
Depth (T)	$O(d \cdot \sqrt{n})$	$O(d \cdot \log^2 n)$	$O(d)$
Qubits (S)	$O(n)$	$O(n \cdot \log n)$	$O(n \cdot \log n)$
Cost (TS)	$O(d \cdot n^{\frac{3}{2}})$	$O(d \cdot n \cdot \log^3 n)$	$O(d \cdot n \cdot \log n)$
Total (TS)	$O(d \cdot n^{\frac{3}{2}})$ dominate		



Challenge 3: High Complexity in the Previous Design



Cost Complexity

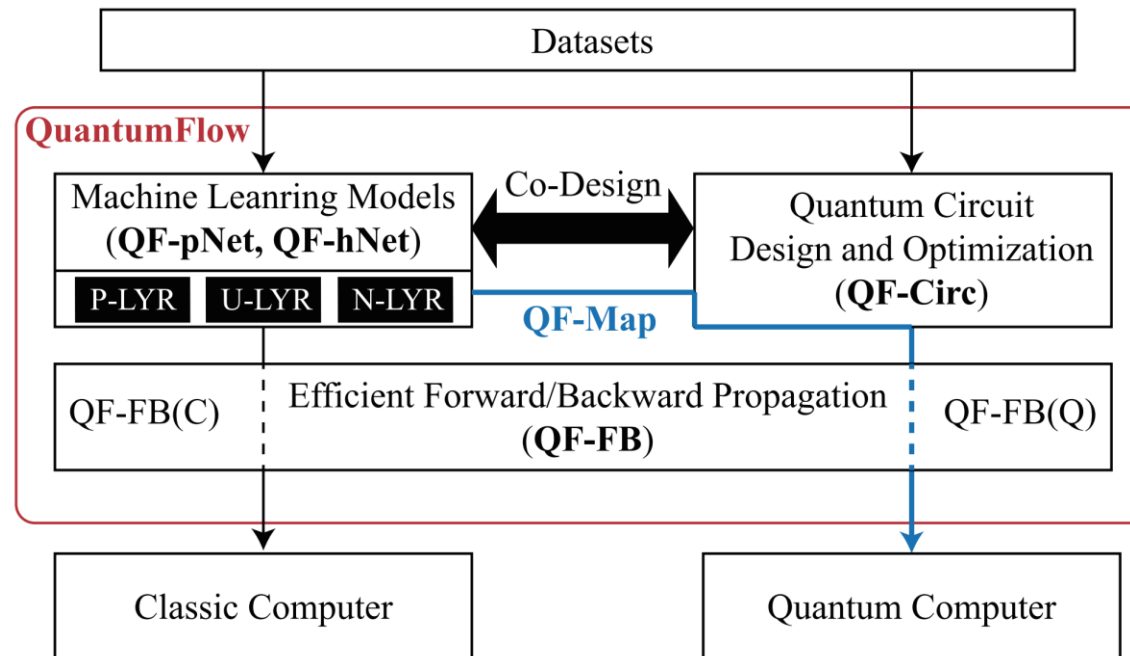
Classical Computing		
	No Parallelism	Full Parallelism
Time (T)	$O(N)$	$O(1)$
Space (S)	$O(1)$	$O(N)$
Cost (TS)	$O(N)$	$O(N)$

Quantum Computing		
	Previous Design	Optimization
Circuit Depth (T)	$O(N)$???
Qubits (S)	$O(\log N)$	$O(\log N)$
Cost (TS)	$O(N \cdot \log N)$	target $O(\text{polylog } N)$

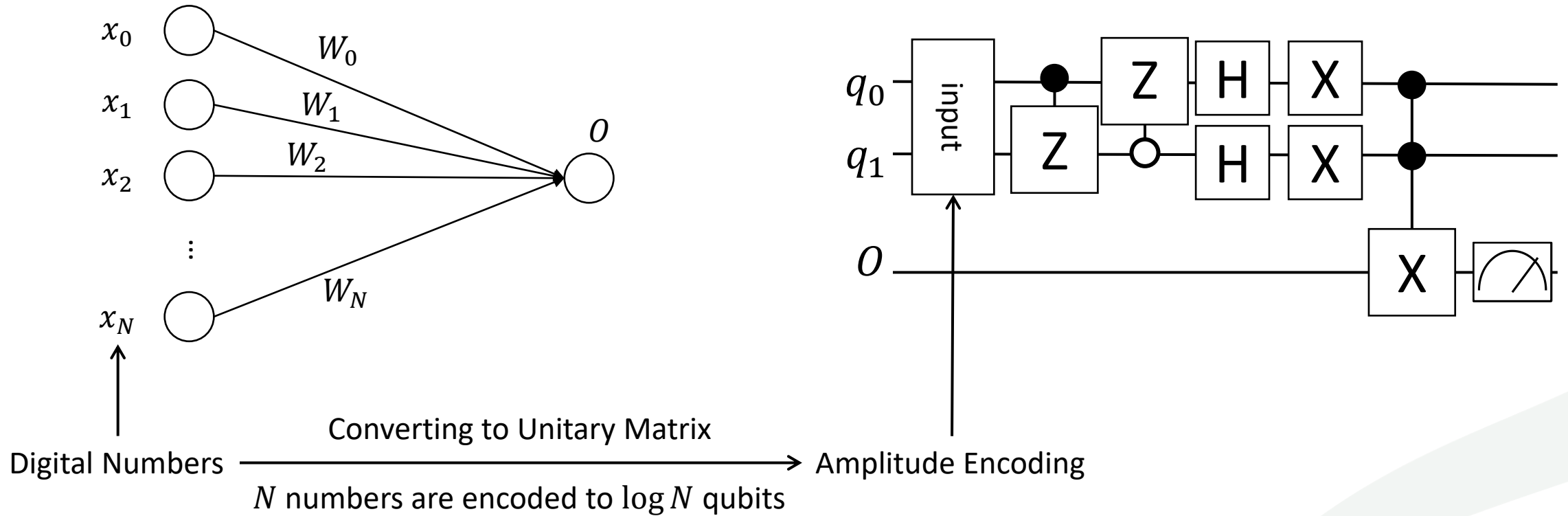
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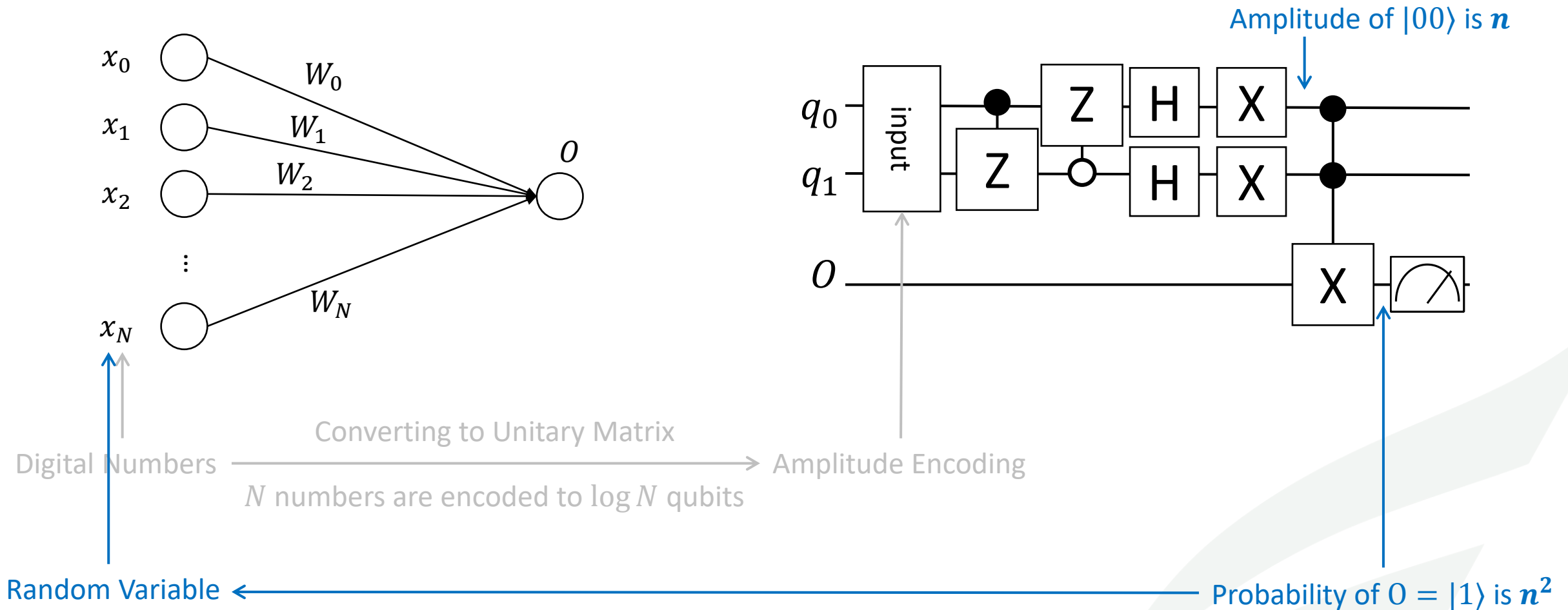
Co-Design Framework



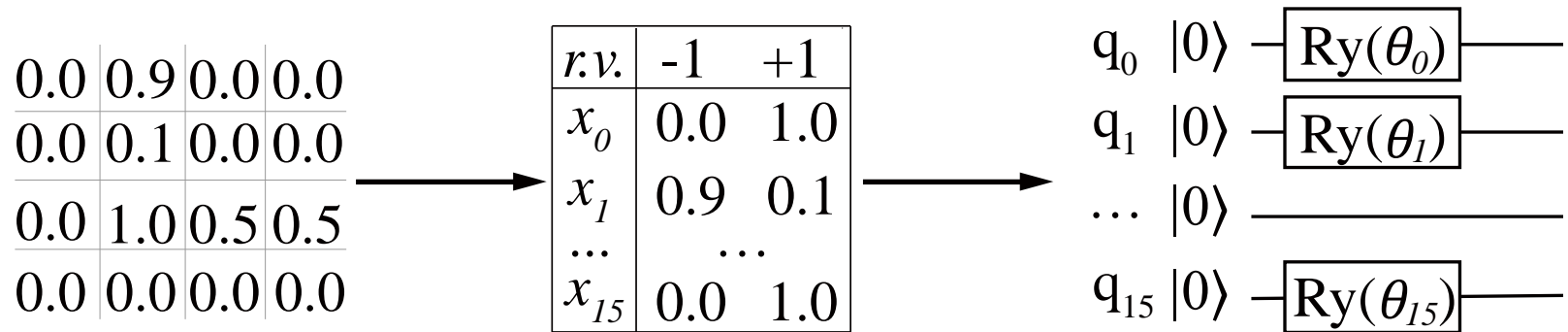
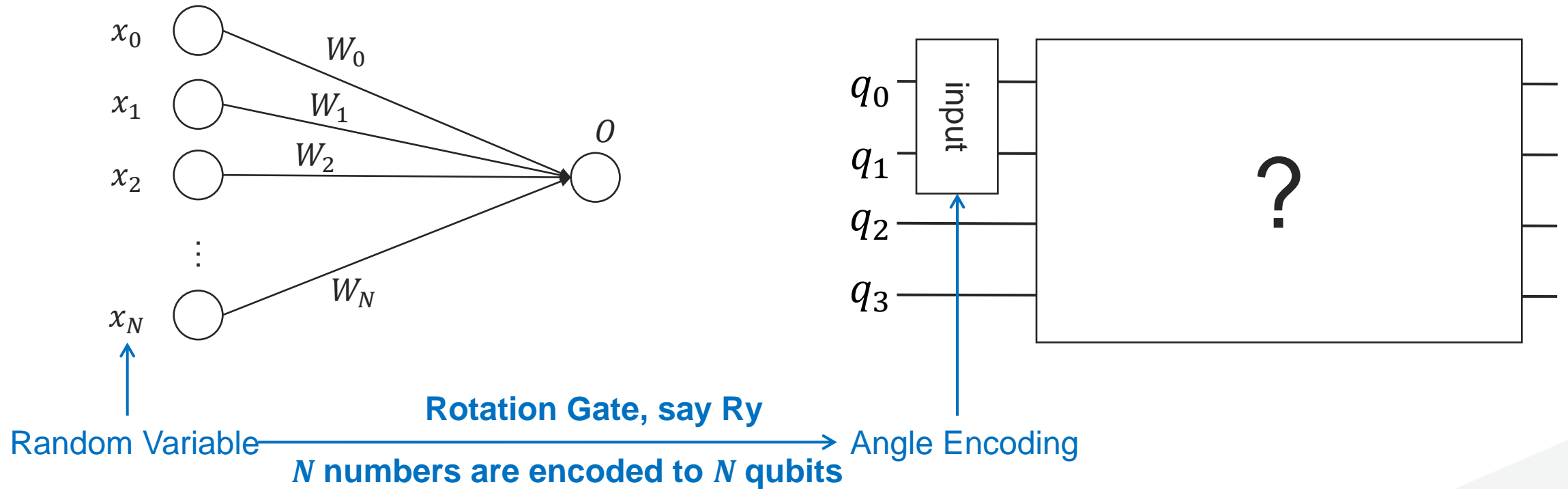
Design Direction 1: NN \rightarrow Quantum Circuit



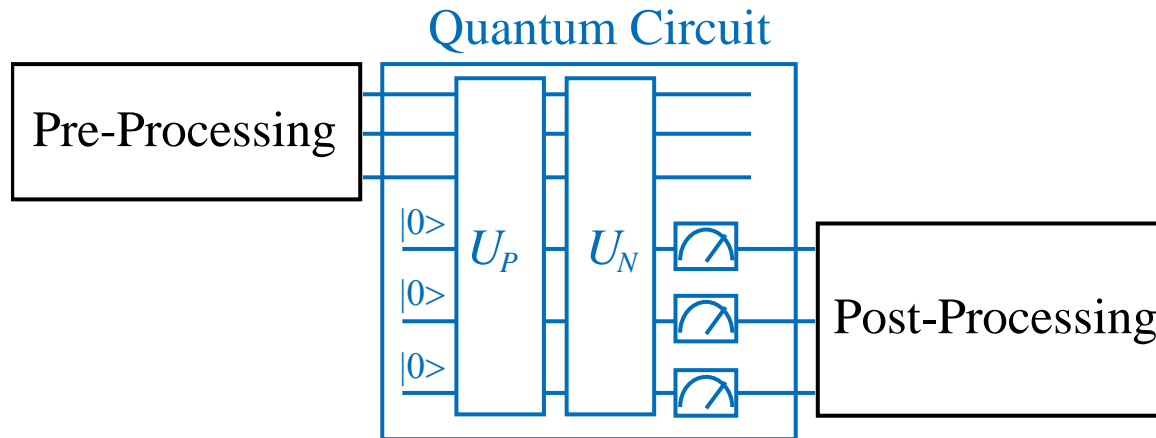
Design Direction 2: Quantum Circuit → NN



Design Direction 3: NN → Quantum Circuit



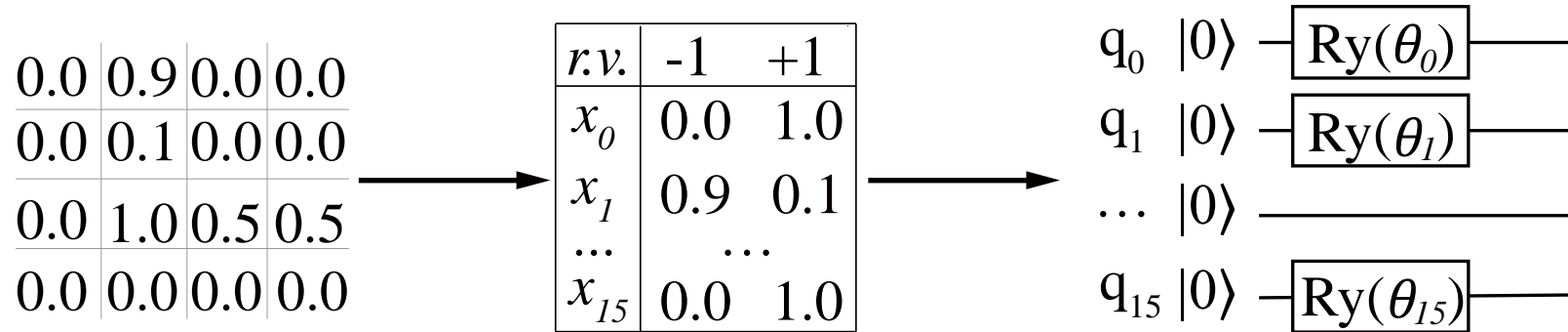
Apply Our Framework to Address Challenges 1 & 2 (non-linear & Q-C comm.)



- (1) Data Pre-Processing (*PreP*)
- (2) HW/Quantum Acceleration
 - (2.1) rvU_p Quantum-State-Preparation
 - (2.2) rvU_N Quantum Neural Computation
 - (2.3) M Measurement
- (3) Data Post-Processing (*PostP*)

rvU_P --- Data Encoding / Quantum State Preparation

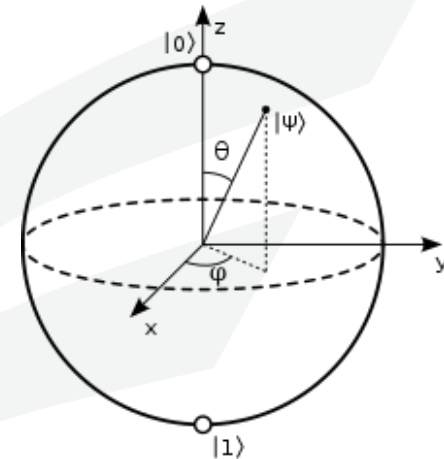
- **Given:** A vector of input data, ranging from [0,1] (do scaling in PreP if range out of [0,1])
- **Do:** Applying rotation gate to encode each data to one qubits
- **Output:** A quantum circuit, where the probability of each qubit to be $|1\rangle$ is the same as the corresponding input data



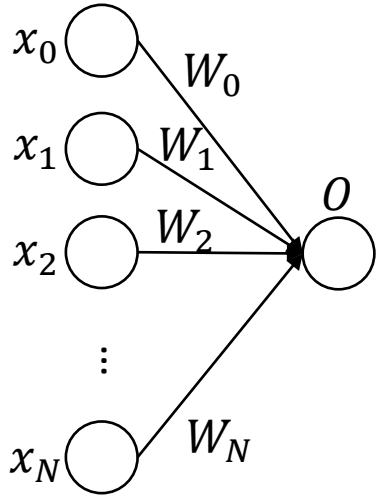
Determination of θ_i :

$$\theta_i = 2 \times \arcsin(\sqrt{x_i})$$

$$|\psi\rangle = \cos\frac{\theta}{2} |0\rangle + (\cos\phi + i \cdot \sin\phi) \cdot \sin\frac{\theta}{2} |1\rangle$$



rvU_N --- Neural Computation



- **Given:** (1) A circuit with encoded input data x ; (2) the trained binary weights w for one neural computation, which will be associated to each data.
- **Do:** Place quantum gates on qubits, such that it performs $\frac{(x*w)^2}{\|x\|^2}$, where x are random variables

$$\text{Target: } O = \left[\frac{\sum_i (x_i \times w_i)}{\|x\|} \right]^2$$

- **Assumption 1:** Parameters/weights ($W_0 \dots W_N$) are binary weight, either +1 or -1
- **Assumption 2:** The weight $W_0 = +1$, otherwise we can use $-w$ (quadratic func.)

$$\text{Step 1: } m_i = x_i \times w_i$$

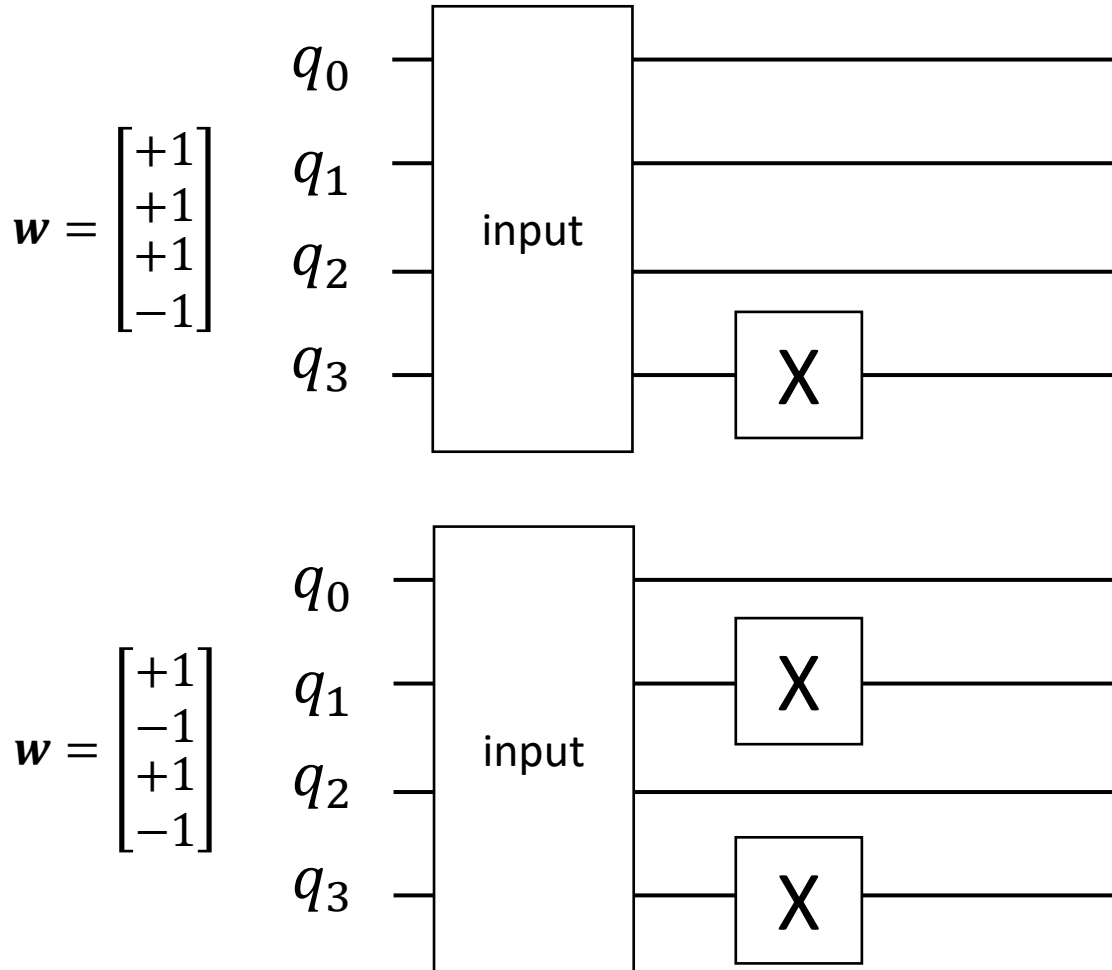
$$\text{Step 2: } n = \left[\frac{\sum_i (m_i)}{\|x\|} \right]$$

$$\text{Step 3: } O = n^2$$

rvU_N --- Neural Computation: Step 1

Step 1: $m_i = x_i \times w_i$

EX: 4 input data on 4 qubits



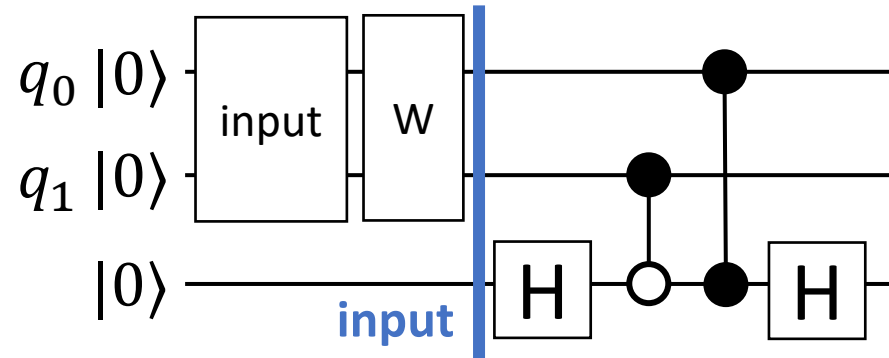
rvU_N --- Neural Computation: Step 2

Step 2: $n = \left\lfloor \frac{\sum_i(m_i)}{\|x\|} \right\rfloor$

EX: 2 input data on 2 qubits

r.v.	-1 ($ 1\rangle$)	+1 ($ 0\rangle$)
m_0	p_0	q_0
m_1	p_1	q_1

r.v.	-1	0	+1
n	p_0p_1	$p_0q_1 + p_1q_0$	q_0q_1



Input

$\sqrt{q_0q_1}$	$ 000\rangle$
0	$ 001\rangle$
$\sqrt{q_0p_1}$	$ 010\rangle$
0	$ 011\rangle$
$\sqrt{p_0q_1}$	$ 100\rangle$
0	$ 101\rangle$
$\sqrt{p_0p_1}$	$ 110\rangle$
0	$ 111\rangle$

IH+CZs

$\sqrt{q_0q_1}/\sqrt{2}$	$ 000\rangle$
$\sqrt{q_0q_1}/\sqrt{2}$	$ 001\rangle$
$-\sqrt{q_0p_1}/\sqrt{2}$	$ 010\rangle$
$\sqrt{q_0p_1}/\sqrt{2}$	$ 011\rangle$
$\sqrt{p_0q_1}/\sqrt{2}$	$ 100\rangle$
$-\sqrt{p_0q_1}/\sqrt{2}$	$ 101\rangle$
$-\sqrt{p_0p_1}/\sqrt{2}$	$ 110\rangle$
$-\sqrt{p_0p_1}/\sqrt{2}$	$ 111\rangle$

IH

$\sqrt{q_0q_1}$	$ 000\rangle$
---	$ 100\rangle$
0	$ 001\rangle$
---	$ 101\rangle$
0	$ 010\rangle$
---	$ 110\rangle$
$-\sqrt{p_0p_1}$	$ 011\rangle$
---	$ 111\rangle$

rvU_N --- Neural Computation: Step 3

Classical:

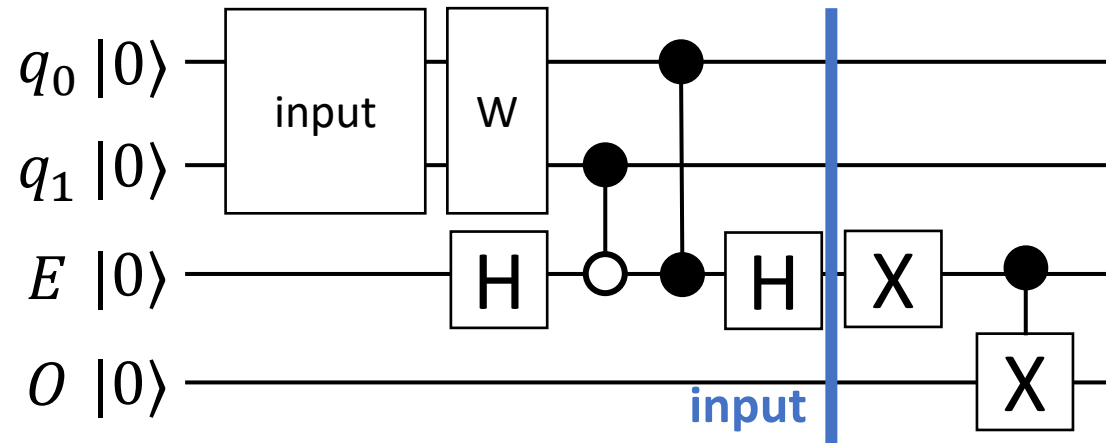
$$E(O) = E(n^2) \\ = 0 \times (p_0q_1 + p_1q_0) + 1 \times (q_0q_1 + p_0p_1)$$

Step 3: $O = n^2$

r.v.	-1	0	+1
n	p_0p_1	$p_0q_1 + p_1q_0$	q_0q_1

r.v.	0	+1
n^2	$p_0q_1 + p_1q_0$	$q_0q_1 + p_0p_1$

EX: 2 input data on 2 qubits



Input

$\sqrt{q_0q_1}$	$ 000\rangle$
---	$ 001\rangle$
0	$ 010\rangle$
---	$ 011\rangle$
0	$ 100\rangle$
---	$ 101\rangle$
$-\sqrt{p_0p_1}$	$ 110\rangle$
---	$ 111\rangle$

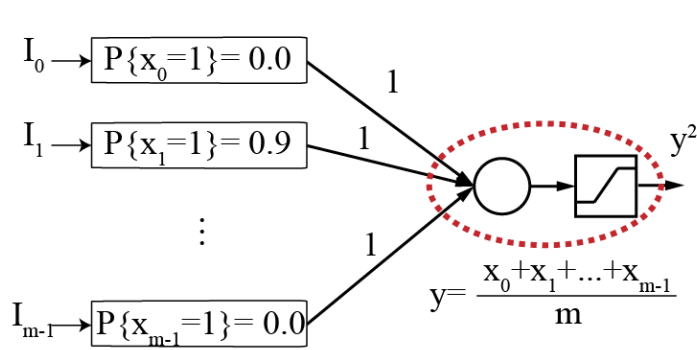
IIX

---	$ 000\rangle$
$\sqrt{q_0q_1}$	$ 001\rangle$
---	$ 010\rangle$
0	$ 011\rangle$
---	$ 100\rangle$
0	$ 101\rangle$
---	$ 110\rangle$
$-\sqrt{p_0p_1}$	$ 111\rangle$

Quantum:

$$P(E = |1\rangle) \\ = \sqrt{q_1q_0}^2 + (-\sqrt{p_1p_0})^2 \\ = q_0q_1 + p_0p_1$$

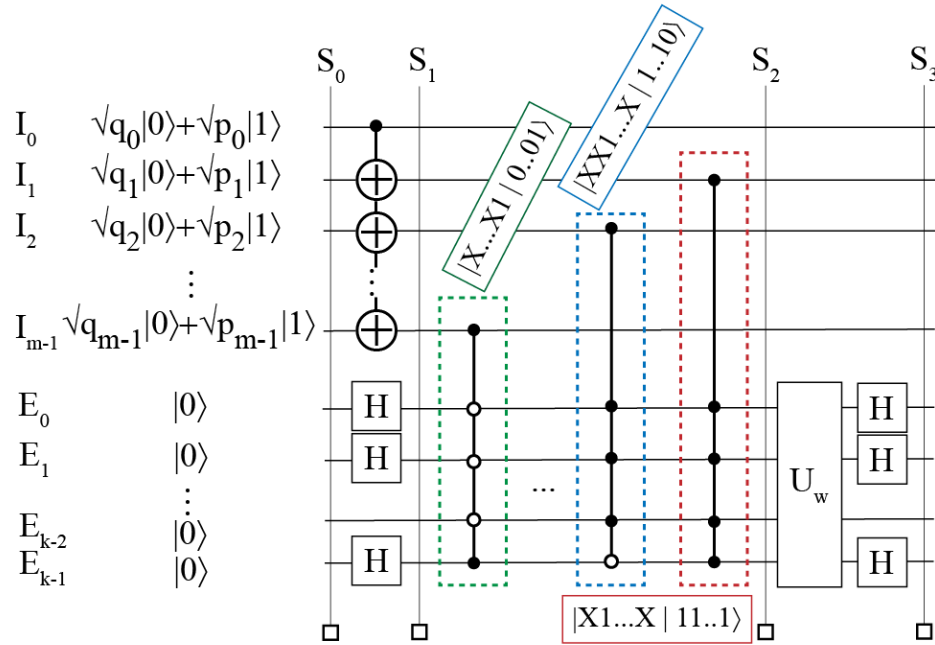
rvU_N --- Neural Computation



	$ 1\rangle$	$ 0\rangle$
	-1	+1
x_0	p_0	q_0
x_1	p_1	q_1
\vdots		
x_{m-1}	p_{m-1}	q_{m-1}

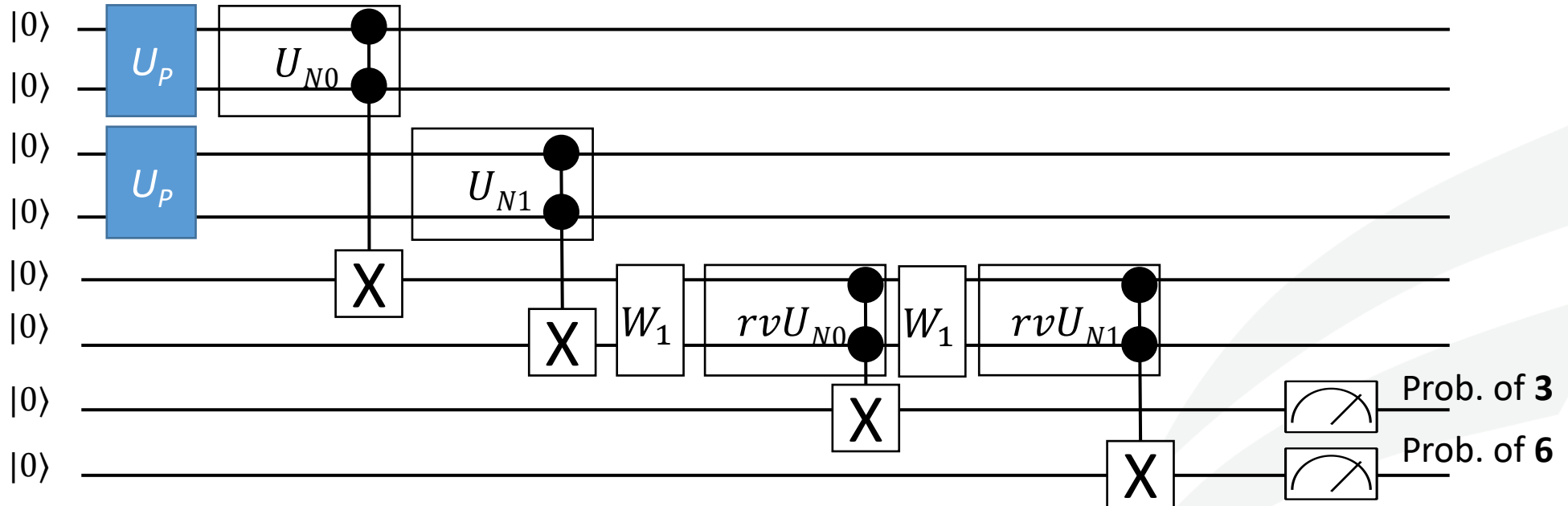
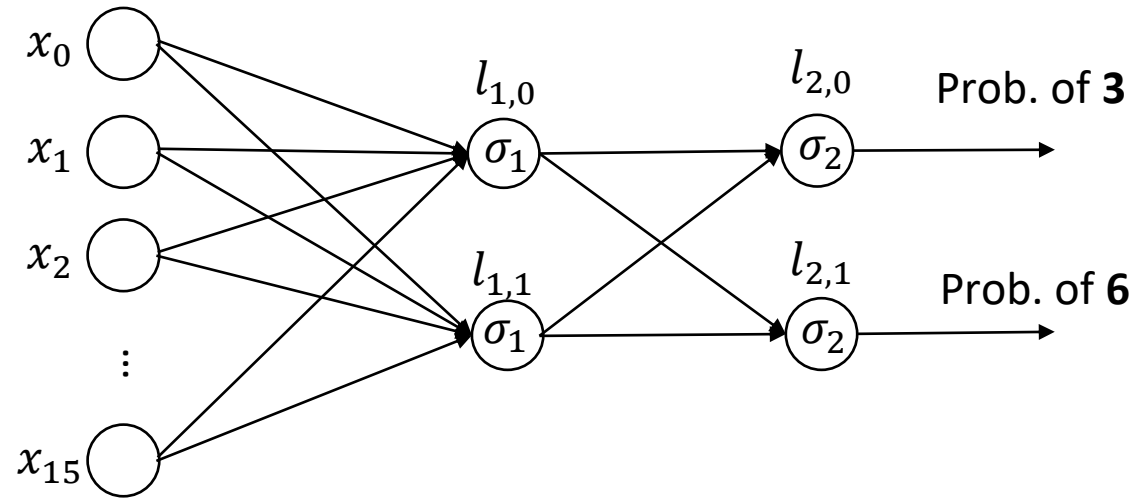
y	-1	$\frac{-m+2}{m}$	\dots	0	\dots	$\frac{m-2}{m}$	1
	$\prod p_i$	$p_{m-1} \dots p_1 q_0$		$q_{m-1} \dots q_1 p_0$		$\prod q_i$	
		$+ p_{m-1} \dots q_1 p_0$		$+ q_{m-1} \dots p_1 q_0$			
		$+ \dots$		$+ \dots$			
		$+ \dots$		$+ \dots$			
		$+ q_{m-1} \dots p_1 p_0$		$+ p_{m-1} \dots q_1 q_0$			

y^2	0	\dots	$(\frac{m-2}{m})^2$	1
			$p_{m-1} \dots p_1 q_0$	$q_{m-1} \dots q_1 p_0$
			$+ p_{m-1} \dots q_1 p_0$	$+ q_{m-1} \dots p_1 q_0$
			$+ \dots$	$+ \dots$
			$+ q_{m-1} \dots p_1 p_0$	$+ p_{m-1} \dots q_1 q_0$



m-k Encoder States	Amplitude			
	S_0	S_1	S_2	S_3
$ 00\dots0\rangle \otimes 0..0\rangle$	$\sqrt{q_{m-1}q_{m-2}\dots q_0}$	$\frac{1}{2^{k/2}} \sqrt{q_{m-1}q_{m-2}\dots q_0}$	$\frac{1}{2^{k/2}} \sqrt{q_{m-1}q_{m-2}\dots q_0}$	$\sqrt{q_{m-1}q_{m-2}\dots q_0}$
$ 00\dots0\rangle \otimes 0..1\rangle$	0	\dots	\dots	XXXXXXXXXX
\dots	\dots	\dots	\dots	\dots
$ 00\dots0\rangle \otimes 1..1\rangle$	0	$\sqrt{q_{m-1}q_{k-1}\dots q_0}$	$\sqrt{q_{m-1}q_{m-2}\dots q_0}$	XXXXXXXXXX
$ 00\dots1\rangle \otimes 0..0\rangle$	$\sqrt{q_{m-1}q_{m-2}\dots p_0}$	$\frac{1}{2^{k/2}} \sqrt{q_{m-1}q_{m-2}\dots p_0}$	$\frac{1}{2^{k/2}} \sqrt{q_{m-1}q_{m-2}\dots p_0}$	$(m-2)/m \sqrt{q_{m-1}q_{m-2}\dots p_0}$
$ 00\dots1\rangle \otimes 0..1\rangle$	0	\dots	$-\sqrt{q_{m-1}q_{m-2}\dots p_0}$	XXXXXXXXXX
\dots	\dots	\dots	\dots	\dots
$ 00\dots1\rangle \otimes 1..1\rangle$	0	$\sqrt{q_{m-1}q_{m-2}\dots p_0}$	$\sqrt{q_{m-1}q_{m-2}\dots p_0}$	XXXXXXXXXX
\dots	\dots	\dots	\dots	\dots
$ 11\dots1\rangle \otimes 0..0\rangle$	$\sqrt{p_{m-1}p_{m-2}\dots p_0}$	$\frac{1}{2^{k/2}} \sqrt{p_{m-1}q_{m-2}\dots q_0}$	$\frac{1}{2^{k/2}} \sqrt{p_{m-1}q_{m-2}\dots q_0}$	$(2-m)/m \sqrt{q_{m-1}q_{m-2}\dots p_0}$
$ 11\dots1\rangle \otimes 0..1\rangle$	0	\dots	$-\sqrt{p_{m-1}q_{m-2}\dots q_0}$	XXXXXXXXXX
\dots	\dots	\dots	\dots	\dots
$ 11\dots1\rangle \otimes 1..1\rangle$	0	$\sqrt{p_{m-1}q_{m-2}\dots q_0}$	$-\sqrt{p_{m-1}q_{m-2}\dots q_0}$	XXXXXXXXXX

Implementing Feedforward Net w/ Non-Linearity, w/o Measurement!



Hands-On Tutorial (3)

PreP+ U_p + U_N + M+ PostP (MNIST)

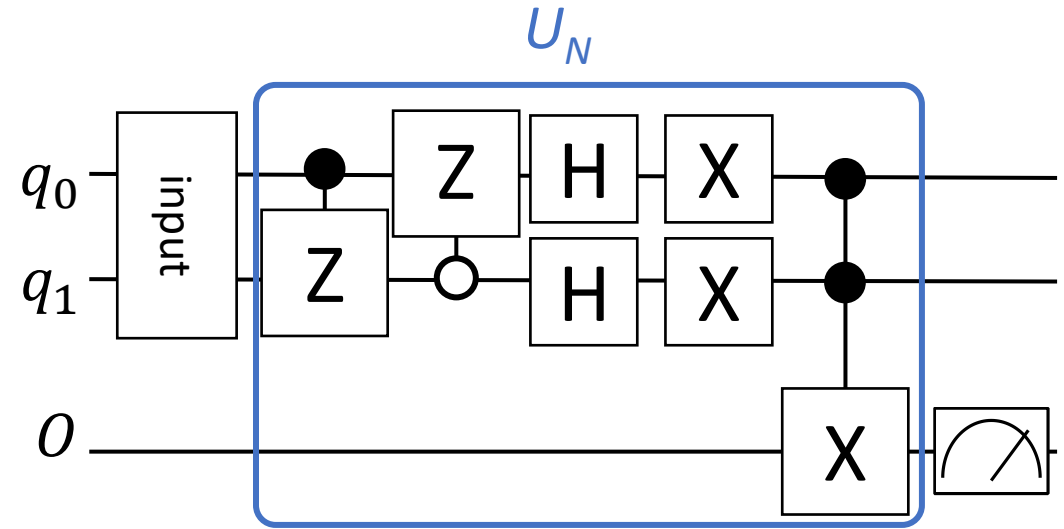
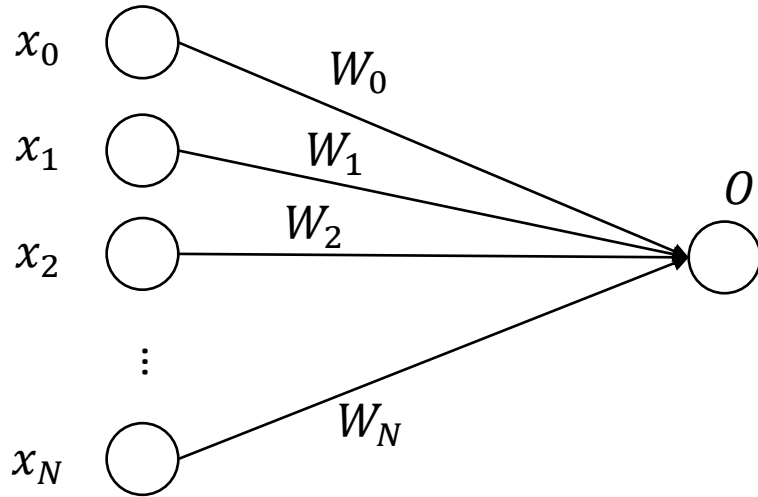


<https://jqub.ece.gmu.edu/categories/QFV/>

Agenda – Session 2: QuantumFlow

- General Framework for Quantum-Based Neural Network Accelerator
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Challenge 3: High Complexity in the Previous Design



Cost Complexity

Classical Computing		
	No Parallelism	Full Parallelism
Time (T)	$O(N)$	$O(1)$
Space (S)	$O(1)$	$O(N)$
Cost (TS)	$O(N)$	$O(N)$

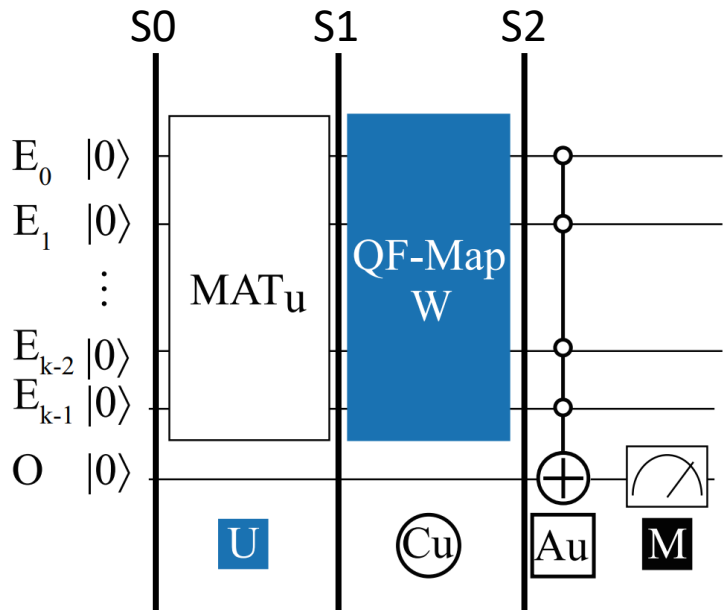
Quantum Computing		
	Previous Design	Optimization
Circuit Depth (T)	$O(N)$???
Qubits (S)	$O(\log N)$	$O(N)$ <i>log N</i>
Cost (TS)	$O(N \cdot \log N)$	target $O(\text{polylog } N)$

QuantumFlow: Taking NN Property to Design QC

$$[0, 0.9, 0, 0, 0, 0, 0.1, 0, 0, 1.0, 0.5, 0.5, 0, 0, 0, 0]^T$$

$$U \downarrow$$

$$[0, 0.59, 0, 0, 0, 0, 0.07, 0, 0, 0.66, 0.33, 0.33, 0, 0, 0, 0]^T$$



S0 -> S1:

$$(v_0; v_{x1}; v_{x2}; \dots; v_{xn}) \times \begin{pmatrix} 1 \\ 0 \\ \dots \\ 0 \end{pmatrix} = (v_0)$$

$$S1 = [0, 0.59, 0, 0, 0, 0, 0.07, 0, 0, 0.66, 0.33, 0.33, 0, 0, 0, 0]^T$$

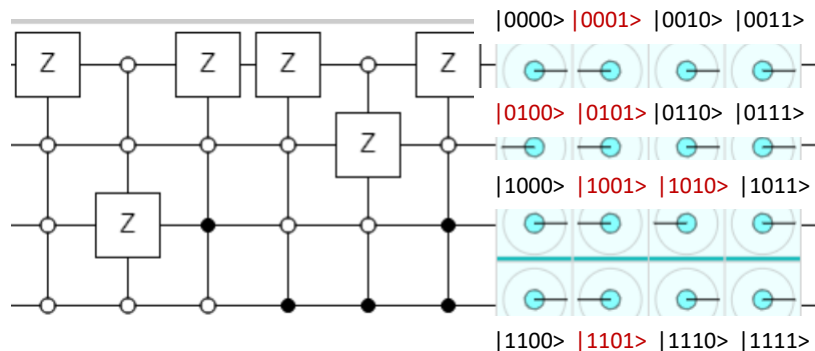
S1 -> S2:

$$W = [+1, -1, +1, +1, -1, -1, +1, +1, +1, -1, -1, +1, +1, -1, +1, +1]^T$$

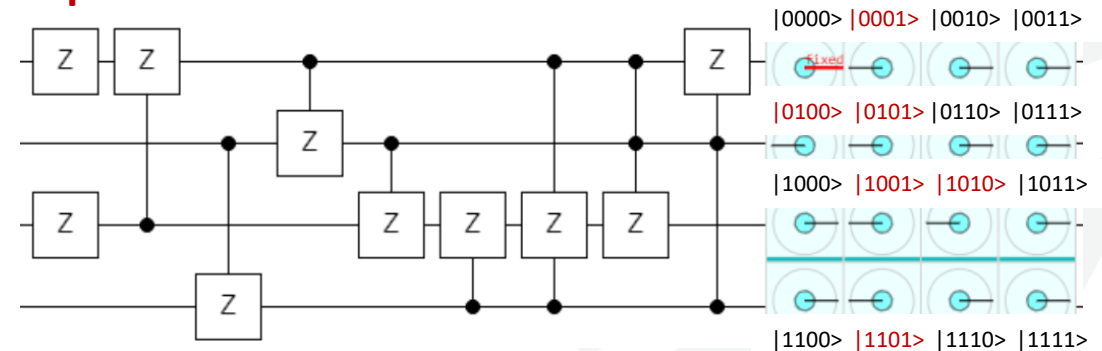
$$|0000\rangle |0001\rangle |0010\rangle |0011\rangle |0100\rangle |0101\rangle |0110\rangle |0111\rangle |1000\rangle |1001\rangle |1010\rangle |1011\rangle |1100\rangle |1101\rangle |1110\rangle |1111\rangle$$

$$S2 = [0, -0.59, 0, 0, -0, -0.07, 0, 0, 0, -0.66, -0.33, 0.33, 0, -0, 0, 0]^T$$

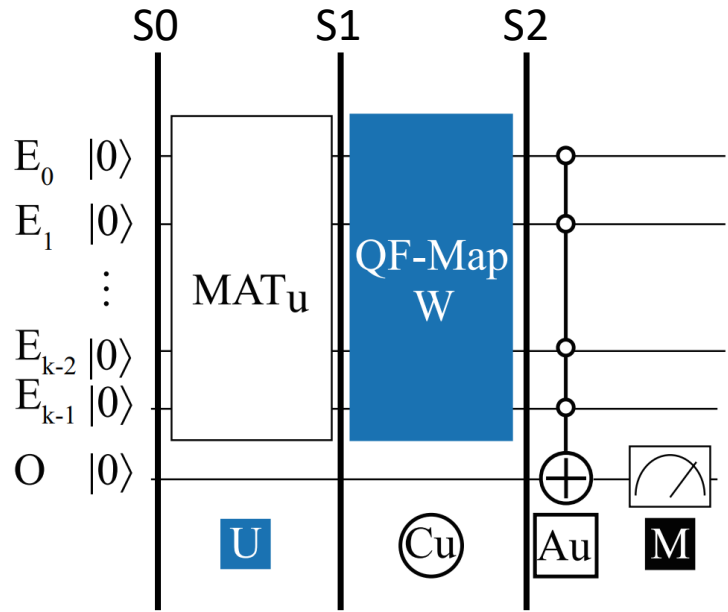
Implementation 1 (example in Quirk):



Implementation 2:

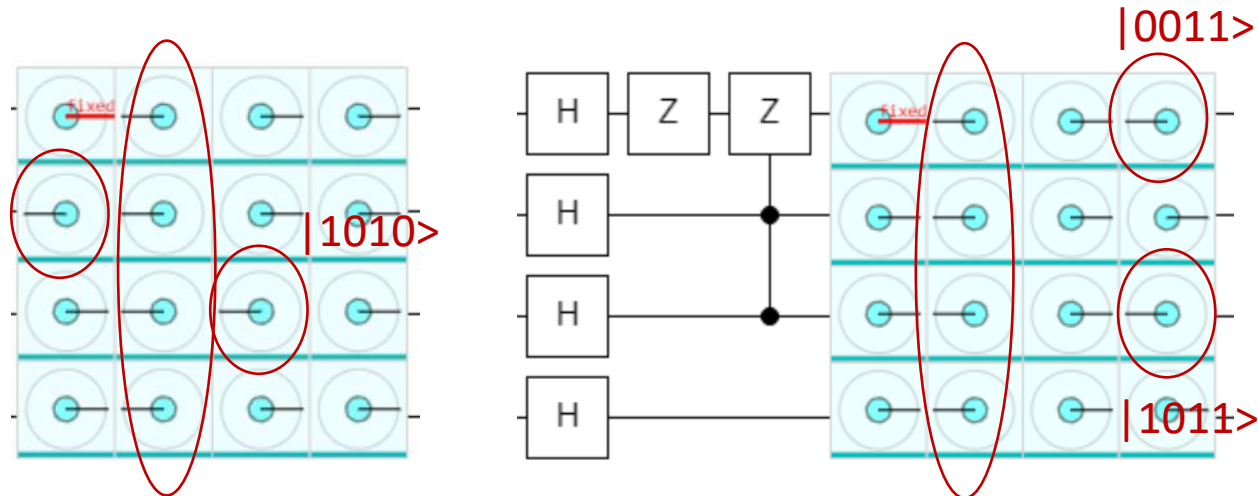


QuantumFlow: Taking NN Property to Design QC



Property from NN

- The **weight order** is not necessary to be fixed, which can be adjusted if the order of inputs are adjusted accordingly
- Benefit:** No need to require the positions of sign flip are exactly the same with the weights; instead, only need the number of signs are the same.

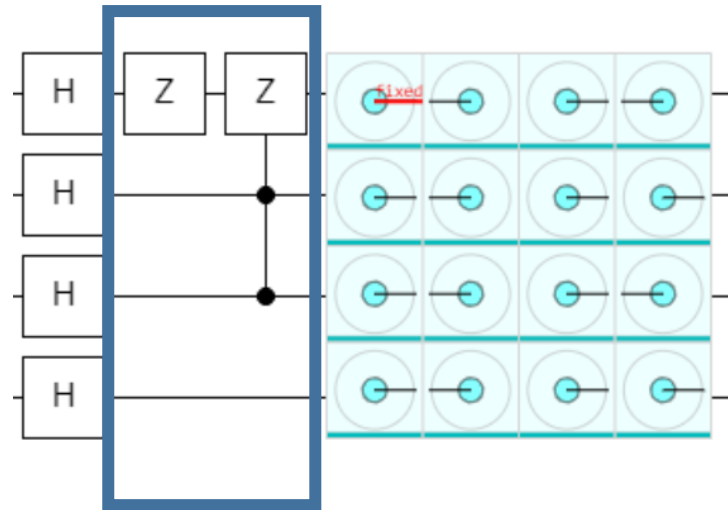
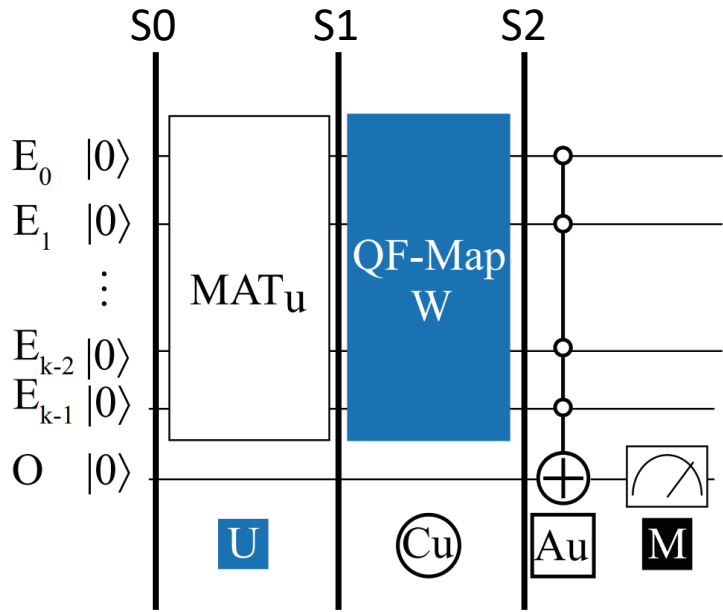


$$S1 = [0, 0.59, 0, \mathbf{0}, \mathbf{0}, 0.07, 0, 0, 0.66, \mathbf{0.33}, \mathbf{0.33}, 0, 0, 0, 0]^T$$

ori		+	-						-	+				
fin				-	+						+	-		

$$S1' = [0, 0.59, 0, \mathbf{0.33}, \mathbf{0.33}, 0.07, 0, 0, 0.66, \mathbf{0}, \mathbf{0}, 0, 0, 0, 0]^T$$

QuantumFlow: Taking NN Property to Design QC



Algorithm 4: QF-Map: weight mapping algorithm

Input: (1) An integer $R \in (0, 2^{k-1}]$; (2) number of qubits k ;

Output: A set of applied gate G

```

void recursive(G,R,k){
    if (R < 2^{k-2}){
        recursive(G,R,k - 1); // Case 1 in the third step
    }
    else if (R == 2^{k-1}){
        G.append(PG_{2^{k-1}}); // Case 2 in the third step
        return;
    }else{
        G.append(PG_{2^{k-1}});
        recursive(G,2^{k-1} - R,k - 1); // Case 3 in the third step
    }
}
// Entry of weight mapping algorithm
set main(R,k){
    Initialize empty set G;
    recursive(G,R,k);
    return G
}
    
```

Used gates and Costs

Gates	Cost
Z	1
CZ	1
C ² Z	3
C ³ Z	5
C ⁴ Z	6
...	...
C ^k Z	2k-1

Worst case: all gates

O(log²N)

Hands-On Tutorial (4)

PreP + U_p + Optimized U_N + M + PostP (MNIST)

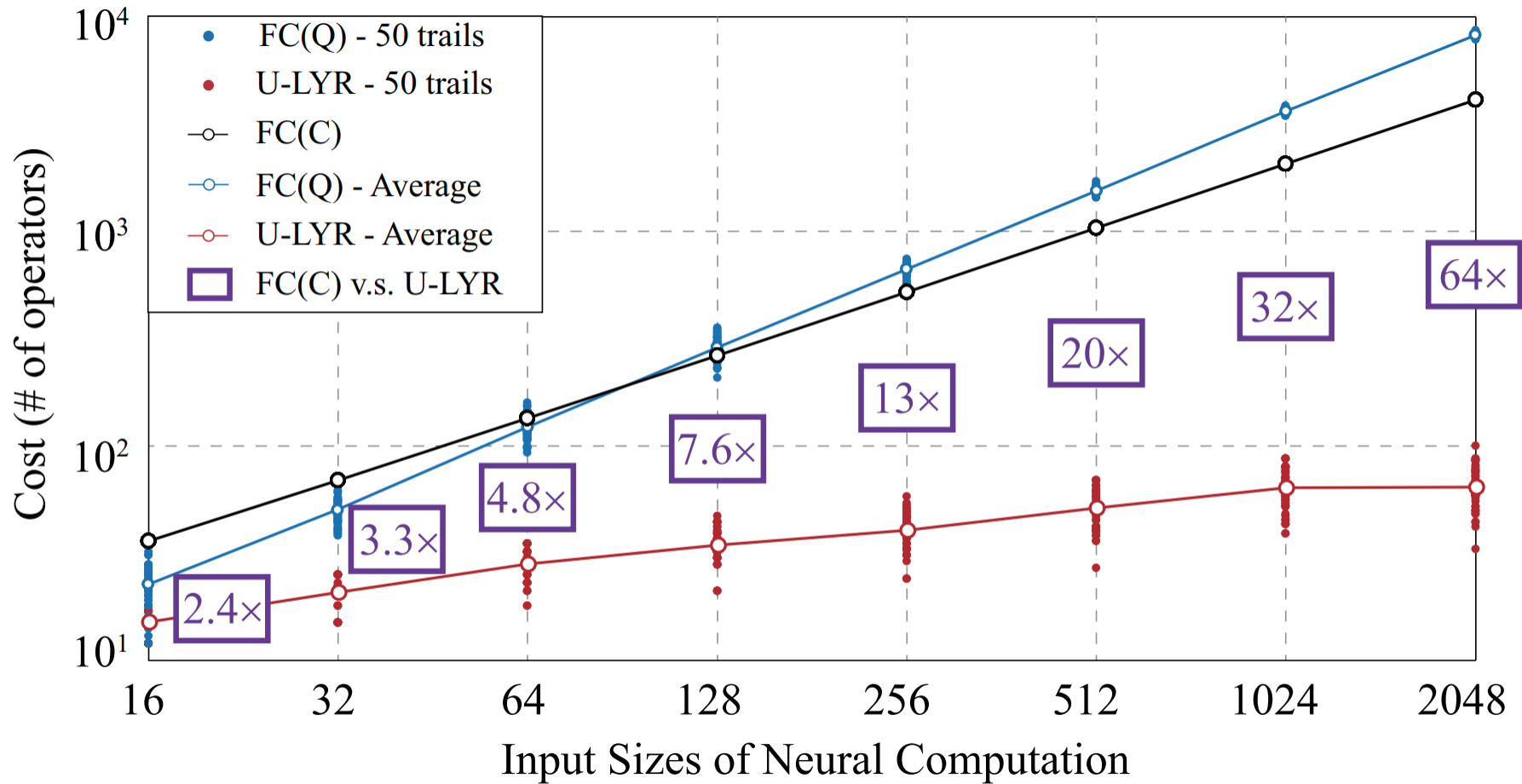


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QuantumFlow Results



[ref] Tacchino, F., et al., 2019. An artificial neuron implemented on an actual quantum processor. *npj Quantum Information*, 5(1), pp.1-8.

QuantumFlow Achieves Over 10X Cost Reduction

Dataset	Structure			MLP(C)			FFNN(Q)				QF-hNet(Q)			
	In	L1	L2	L1	L2	Tot.	L1	L2	Tot.	Red.	L1	L2	Tot.	Red.
{1,5}	16	4	2				80	38	118	1.27 ×	74	38	112	1.34 ×
{3,6}	16	4	2				96	38	134	1.12 ×	58	38	96	1.56 ×
{3,8}	16	4	2	132	18	150	76	34	110	1.36 ×	58	34	92	1.63 ×
{3,9}	16	4	2				98	42	140	1.07 ×	68	42	110	1.36 ×
{0,3,6}	16	8	3				173	175	348	0.91 ×	106	175	281	1.12 ×
{1,3,6}	16	8	3	264	51	315	209	161	370	0.85 ×	139	161	300	1.05 ×
{0,3,6,9}	64	16	4	2064	132	2196	1893	572	2465	0.89 ×	434	572	1006	2.18 ×
{0,1,3,6,9}	64	16	5				1809	645	2454	0.91 ×	437	645	1082	2.06 ×
{0,1,2,3,4}	64	16	5	2064	165	2229	1677	669	2346	0.95 ×	445	669	1114	2.00 ×
{0,1,3,6,9}*	256	8	5	4104	85	4189	5030	251	5281	0.79 ×	135	251	386	10.85 ×

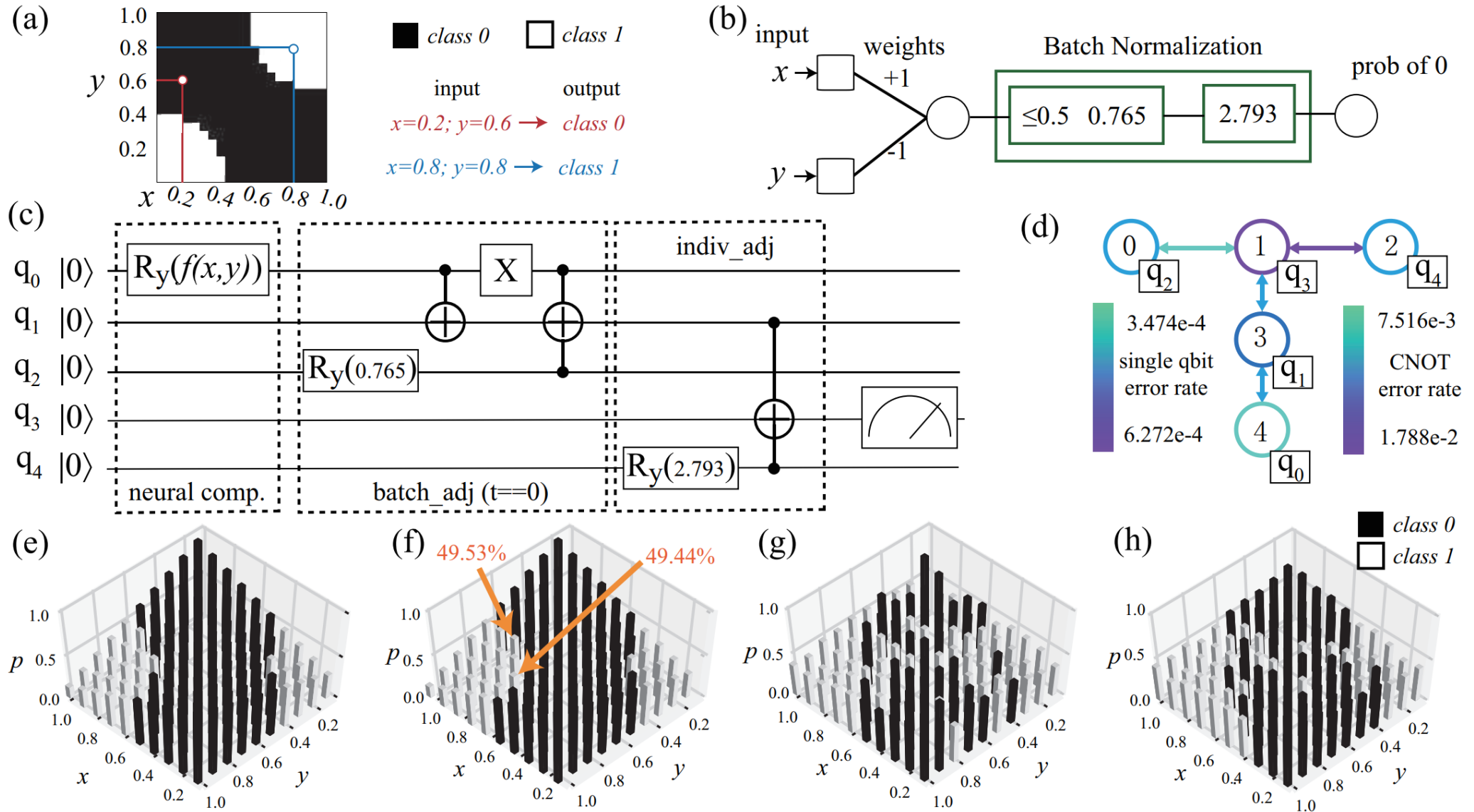
*: Model with 16×16 resolution input for dataset {0,1,3,6,9} to test scalability, whose accuracy is 94.09%, which is higher than 8×8 input with accuracy of 92.62%.

QF-Nets Achieve the Best Accuracy on MNIST

Dataset	w/o BN					w/ BN				
	binMLP(C)	FFNN(Q)	MLP(C)	QF-pNet	QF-hNet	binMLP(C)	FFNN(Q)	MLP(C)	QF-pNet	QF-hNet
1,5	61.47%	61.47%	69.12%	69.12%	90.33%	55.99%	55.99%	85.30%	84.56%	96.60%
3,6	72.76%	72.76%	94.21%	91.67%	97.21%	72.76%	72.76%	96.29%	96.39%	97.66%
3,8	58.27%	58.27%	82.36%	82.36%	89.77%	58.37%	58.07%	86.74%	86.90%	87.20%
3,9	56.71%	56.51%	68.65%	68.30%	95.49%	56.91%	56.71%	80.63%	78.65%	95.59%
0,3,6	46.85%	51.63%	49.90%	59.87%	89.65%	50.68%	50.68%	75.37%	78.70%	90.40%
1,3,6	60.04%	59.97%	53.69%	53.69%	94.68%	59.59%	59.59%	86.76%	86.50%	92.30%
0,3,6,9	72.68%	72.33%	84.28%	87.36%	92.85%	69.95%	68.89%	82.89%	76.78%	93.63%
0,1,3,6,9	50.00%	51.10%	49.00%	43.24%	87.96%	60.96%	69.46%	70.19%	71.56%	92.62%
0,1,2,3,4	46.96%	50.01%	49.06%	52.95%	83.95%	64.51%	69.66%	71.82%	72.99%	90.27%

[ref of FFNN] Tacchino, F., et al., 2019. Quantum implementation of an artificial feed-forward neural network. *arXiv preprint arXiv:1912.12486*.

On Actual IBM “ibmq_essex” Quantum Processor



Hands-On Tutorial (5)

Comparison



<https://jqub.ece.gmu.edu/categories/QFV/>





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