ECE 590-001: Machine Learning for Embedded Systems

Fall 2021

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Class Meeting: Lectures – Monday 4:30 pm – 7:10 pm

Location: Horizon Hall (HORIZON) 2010

Course Materials: Course materials will be posted before or after the class. No formal textbook is required. The book from <u>http://www.deeplearningbook.org/</u> will be referred to on the course.

Prerequisites: CS 222 and ECE 231 and ECE 350 with the minimum grade of C

The course topics are self-contained so that a background in machine learning is not required. Students should be familiar with programming and embedded systems to complete the course projects.

Course Description: Machine learning (ML) has gradually become the core component of wide applications in different computing scenarios, ranging from edge computing to cloud computing. This course focuses on resource-constrained edge computing, in particular the embedded systems, and introduces techniques for developing energy/time efficient ML algorithms and models for the embedded systems. Topics that are covered include (i) commonly used ML algorithms, (ii) ML model compression techniques, (iii) hardware-aware machine learning, (iv) hardware and neural architecture co-design. The course also provides a comprehensive team-based research and development experience through projects and presentations. Offered by <u>Electrical & Comp. Engineering</u>. May not be repeated for credit.

Course Schedule: there are three sections of the course, including:

- Section I: Introduction of Machine Learning and Deep Neural Networks
- Section II: Automated Neural Network Design
- Section III: Optimization of both ML/DNN and Hardware Design

SECTION I: Introduction of Machine Learning and Deep Neural Networks

| Date | Торіс |
|--------|---|
| Week 1 | Course Information & Introduction to Machine Learning |
| Week 2 | Train Neural Networks |
| Week 3 | Deep Convolutional Neural Networks (CNN) |
| Week 4 | Natural Langue Processing |

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SECTION II: Automated Neural Network Design

| Date | Торіс |
|---------|--------------------------------|
| Week 6 | ML Accelerator Design (1) |
| Week 7 | ML Accelerator Design (2) |
| Week 8 | Model Compression |
| Week 9 | Neural Architecture Search (1) |
| Week 10 | Neural Architecture Search (2) |

SECTION III: Optimization of both ML/DNN and Hardware Design

| Date | Торіс |
|---------|---|
| Week 11 | Hardware-Aware Neural Architecture Search |
| Week 12 | HW/SW Co-Design with Neural Architecture Search (1) |
| Week 13 | HW/SW Co-Design with Neural Architecture Search (2) |
| Week 14 | Course Project Demonstration |

* The schedule might change during the semester depending on the progress of the class.

Goals and Outcomes:

- Understand the basic training and inference techniques of a neural network. (Section I)
- Get familiar with commonly used neural networks, such as CNN and RNN (Section I)
- Be able to implement neural networks using machine learning tools (Section I)
- Be able to implement neural networks on customized hardware, such as FPGA (Section II)
- Be able to design the customized neural network (Section II)
- Be able to apply compression techniques, i.e., pruning and quantization (Section II)
- Be able to co-design neural networks and hardware accelerators (Section III)

Homework Labs: There are 5 take-home labs in Section I assigned to the students to practice basic skills of machine learning implementation using TensorFlow or Pytorch. A total of 7 days after the due dates are permitted for all assignments. However, after the due date, each assignment will be deducted 10 points for each day late.

Presentation: The project-oriented research articles will be assigned to the teams of students at the end of Section I. Students then need to prepare a presentation on the assigned research articles in Section II or III.

Project: Students will form teams to implement several open-topic projects in terms of different hardware platforms: mobile devices, FPGAs, or ASICs. According to different projects, open-source or free-version software would be involved, e.g., the TensorFlow Lite for mobile devices and Vivado for FPGA synthesis. Each team will be assigned one project. At the end of the course, students will give a demonstration of the completed projects and deliver a comprehensive project/technical report.

Grading:

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| • | Homework assignments (5 take-home labs, 10%) | 50% |
| ٠ | Research paper presentation | 20% |
| ٠ | Project progress review (team based) | 10% |
| • | Project report (team based) | 20% |

Course and University Policies:

- <u>Email Communications</u>: Students must use their MasonLive email account to receive important University information, including messages related to this class. See http://masonlive.gmu.edu for more information. Homework assignments and other course material will be emailed to your MasonLive email account. Also, when you send me an email, please write ECE590-001 on the subject line.
- <u>Avoid Reposting Course Material</u>: It is not allowed to reposting course material. The course materials (lecture notes, homework, projects, exams, solutions, and anything else posted on the course website) are copyrighted. You may not upload them to any other website or share them with any on-line or off-line test bank.
- <u>Honesty and Integrity</u>: Mason expects students to pursue their academic work with honesty and integrity. Students should feel free to work in groups to discuss lecture material and homework assignments; however, under no circumstance should a student represent another's work as his or her own. Copying solutions for assigned homework problems, from any source, constitutes a violation of the university honor code. Any form of cheating may cause penalties, from getting an F in this course to academic actions in accordance with university policy.
- <u>Office of disability services:</u> Mason provides accommodations through the Office of Disability Services (ODS) http://ods.gmu.edu. If you are a student with a disability and you need academic accommodations, please see me and contact ODS at 993-2474.
- <u>University Policies</u>: The University Catalog, http://catalog.gmu.edu, is the central resource for university policies affecting student, faculty, and staff conduct in

university academic affairs. Other policies are available at http://universitypolicy.gmu.edu/. All members of the university community are responsible for knowing and following established policies.